



VXR125-27000S SERIES

HIGH RELIABILITY COTS DC-DC CONVERTERS



Models Available

Input: 160 V to 400 V continuous, 125 V to 500 V transient
125 W, single output of 3.3 V, 5 V, 12 V, 15 V, 28 V
-55 °C to 105 °C Operation

1.0 DESCRIPTION

The VXR Series of isolated DC-DC converters is the latest and most advanced offering in VPT's extensive line of high reliability COTS products. Building on a proven design heritage, the VXR Series features high efficiency, an ultra-wide input voltage range, and next-generation packaging. Providing a wide 160 V to 400 V input, along with VPT's standard full feature set and industry-leading performance, the VXR family is optimized for a broad range of applications from military ground vehicles to commercial and military aircraft.

The VXR125 utilizes a low noise, fixed frequency, wide input voltage range topology with extended high and low input transient capability to simplify overall power system design and compliance. Precision controlled synchronous rectification achieves high efficiency over a wide input range, reducing power dissipation and minimizing external heatsink requirements. A proprietary control loop design provides fast transient response without the use of optoisolators.

The VXR Series patented epoxy-encapsulated V-SHIELD® packaging is highly resistant to chemical, solvent and salt environments and is fully compatible with high volume manufacturing processes including wave solder, cleaning solvents, high pressure sprays and aqueous wash processes. A unique integral six-sided metalized shield improves system EMI compatibility. Dual sided conduction cooling coupled with reduced power dissipation simplifies system thermal design.

The VXR Series is intended for harsh environments including severe vibration, shock and temperature cycling. Testing is to JESD22, MIL-STD-810, and MIL-STD-883.

1.1 FEATURES

- Wide input voltage range: 160 V to 400 V
- 125 V to 500 V transient operation
- High efficiency up to 90%
- Rugged epoxy encapsulated V-SHIELD® package
- Fully compatible with aqueous cleaning processes
- Integral six-sided metalized EMI shield
- Dual-sided thermal conduction
- 2000 V isolation
- Fixed frequency
- Low noise
- Output voltage trim (+10% / -20%)
- Output current limit protection
- Short circuit protection
- Overvoltage protection

1.2 COMPLIANCE

- MIL-STD-704 A-F
- MIL-STD-461 C-G when used with an appropriate VXR EMI filter
- RTCA / DO-160-Section 16
- RTCA / DO-160-Section 18 and 21 when used with an appropriate VXR EMI filter

1.3 PACKAGING

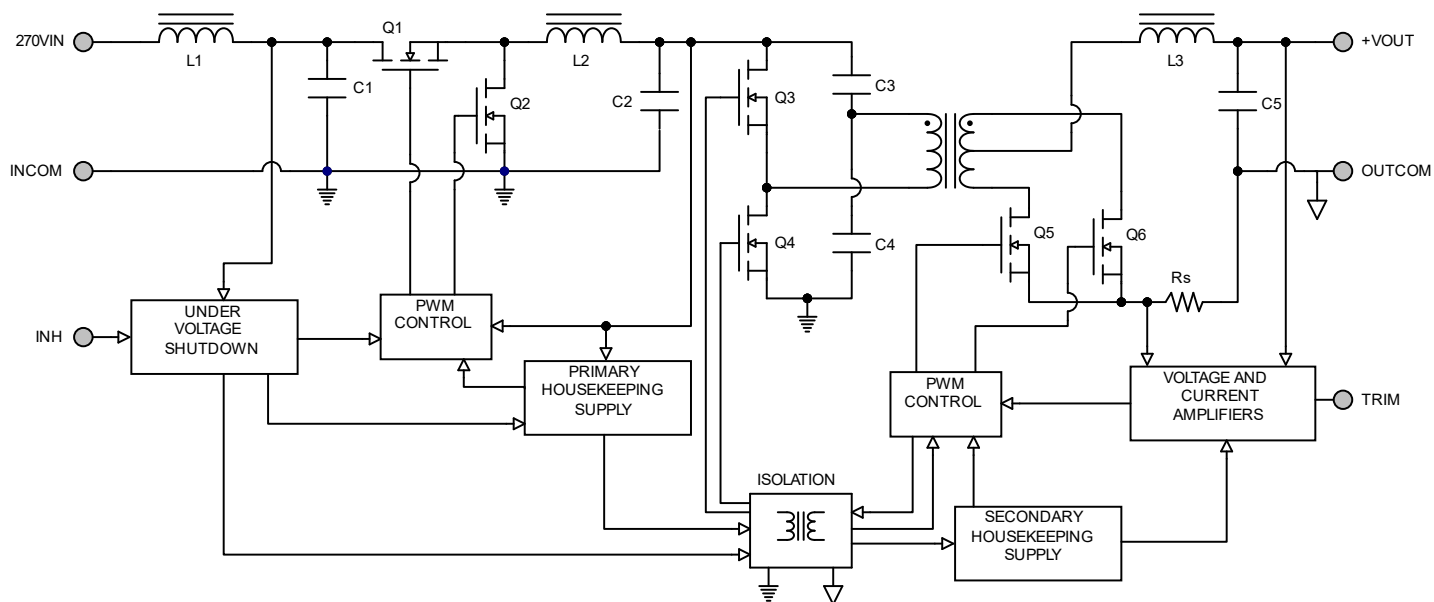
- Low-profile: 2.350" x 1.550" x 0.490"
- Max weight: 88 g

1.4 ACCESSORIES

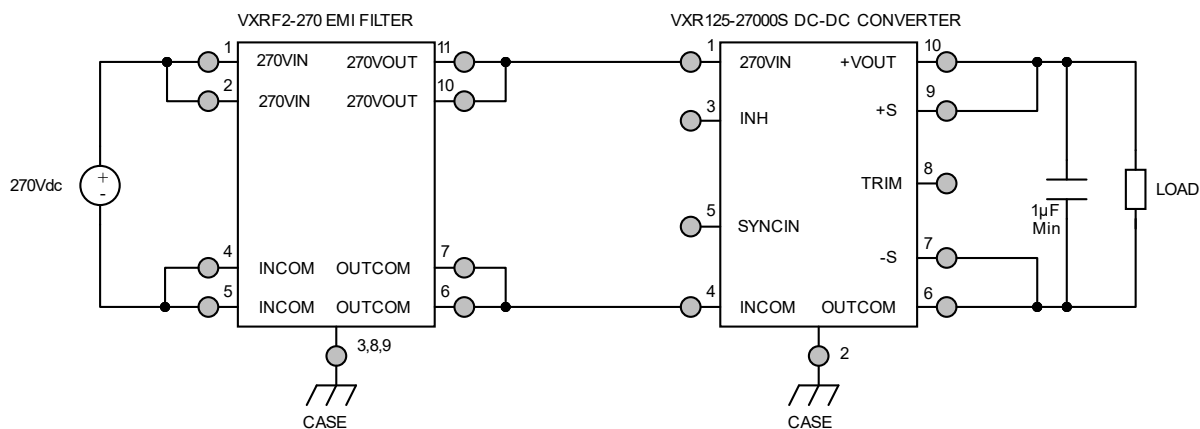
- EMI filter – VXRF2-270, 2A
- Use with Thermal Pad TP-011

2.0 DESCRIPTION

2.1 BLOCK DIAGRAM



2.2 CONNECTION DIAGRAM



3.0 SPECIFICATIONS

3.1 ABSOLUTE MAXIMUM RATINGS

Absolute Maximum Ratings			
Input Voltage (Continuous):	400 V	Operating Temperature (Full Load):	-55 °C to + 105 °C
Input Voltage (Transient, 1 second):	500 V	Storage Temperature:	-55 °C to + 125 °C
		Lead Solder Temperature (10 seconds):	300 °C

3.2 PERFORMANCE SPECIFICATIONS

Tcase = -55 °C to +105 °C, Vin = +270 V ± 5%, Full Load, Unless Otherwise Specified

		VXR125-2703R3S			VXR125-27005S			
Parameter	Conditions	Min	Typ	Max	Min	Typ	Max	Units
INPUT								
Voltage	Continuous	160	270	400	160	270	400	V
	Transient, 1 sec ³	125	-	500	125	-	500	V
Current	INH < 0.2 V	-	2	5	-	2	5	mA
	No Load	-	30	70	-	45	80	mA
Ripple Current	20 Hz to 10 MHz	-	100	220	-	100	250	mApp
Undervoltage Lockout	Turn-On	-	-	159.9	-	-	159.9	V
	Turn-Off ³	-	-	124.9	-	-	124.9	V
OUTPUT STATIC								
Voltage	+Vout, Tcase = 25 °C	3.25	3.3	3.35	4.92	5	5.08	V
	+Vout, Tcase = -55 °C to +105 °C	3.22	3.3	3.38	4.87	5	5.13	V
Power ²		0	-	100	0	-	125	W
Current ²		0	-	30	0	-	25	A
Ripple Voltage ⁴	20 Hz to 10 MHz	-	100	150	-	80	150	mVpp
Line Regulation	V _{IN} = 160 V to 400 V	-	1	20	-	1	20	mV
Load Regulation	No Load to Full Load	-	1	20	-	10	25	mV
Load Fault Power Dissipation	Overload ³	-	18	30	-	18	30	W
	Short Circuit ³	-	19	30	-	19	30	W
OUTPUT DYNAMIC								
Load Step, Half to Full Load	Output Transient	-	65	120	-	65	120	mVpk
	Recovery ¹	-	15	50	-	15	50	μs
Line Step ³ , Vin = 160 V to 400 V	Output Transient	-	40	80	-	60	120	mVpk
	Recovery ¹	-	45	80	-	50	100	μs
Turn-On, Vin = 0 to 270 V	Delay	-	15	25	-	15	25	ms
	Overshoot	-	0	30	-	0	30	mVpk
Input Attenuation ³	V _{OUT} / V _{IN} @ 1.2 kHz	-	80	-	-	80	-	dB
FUNCTION								
INH Pin Input ³	Output Inhibited	0	-	1.5	0	-	1.5	V
INH Pin Open Circuit Voltage ³	Output Enabled	-	9	13	-	9	13	V
Voltage Trim Range		-12	-	10	-20	-	10	%
SYNC Frequency Range		500	-	600	500	-	600	kHz
Overvoltage Trip Point ³		3.8	4.1	4.4	5.75	6.2	6.6	V
GENERAL								
Efficiency		84	88	-	84	89	-	%
Capacitive Load ³		-	-	15000	-	-	15000	μF
Switching Frequency	Input	225	250	275	225	250	275	kHz
	Output	450	500	550	450	500	550	kHz
Isolation	2000 V DC, Tcase = 25 °C	100	-	-	100	-	-	MΩ
Weight		-	-	88	-	-	88	g
MTBF (MIL-HDBK-217F)	GM @ Tcase = 55 °C	-	402	-	-	402	-	kHr

1. Time for output voltage to settle within 1% of steady-state value.

2. Derate linearly to 0 at 115 °C.

3. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

4. With additional 1μF ceramic capacitor from VOUT to OUTCOM.

3.2 PERFORMANCE SPECIFICATIONS (CONTINUED)

Tcase = -55 °C to +105 °C, Vin = +270 V ± 5%, Full Load, Unless Otherwise Specified

		VXR125-27012S			VXR125-27015S			
Parameter	Conditions	Min	Typ	Max	Min	Typ	Max	Units
INPUT								
Voltage	Continuous	160	270	400	160	270	400	V
	Transient, 1 sec ³	125	-	500	125	-	500	V
Current	INH < 0.2 V	-	2	5	-	2	5	mA
	No Load	-	40	80	-	45	85	mA
Ripple Current	20 Hz to 10 MHz	-	110	250	-	110	250	mApp
Undervoltage Lockout	Turn-On	-	-	159.9	-	-	159.9	V
	Turn-Off ³	-	-	124.9	-	-	124.9	V
OUTPUT STATIC								
Voltage	+Vout, Tcase = 25 °C	11.82	12	12.18	14.77	15	15.23	V
	+Vout, Tcase = -55 °C to +105 °C	11.7	12	12.3	14.62	15	15.38	V
Power ²		0	-	125	0	-	125	W
Current ²		0	-	10.4	0	-	8.33	A
Ripple Voltage ⁴	20 Hz to 10 MHz	-	110	200	-	110	200	mVpp
Line Regulation	V _{IN} = 160 V to 400 V	-	1	20	-	1	20	mV
Load Regulation	No Load to Full Load	-	10	30	-	10	30	mV
Load Fault Power Dissipation	Overload ³	-	21	30	-	18	30	W
	Short Circuit ³	-	20	30	-	18	30	W
OUTPUT DYNAMIC								
Load Step, Half to Full Load	Output Transient	-	100	200	-	100	200	mVpk
	Recovery ¹	-	-	70	-	-	70	µs
Line Step ³ , Vin = 160 V to 400 V	Output Transient	-	60	120	-	60	120	mVpk
	Recovery ¹	-	-	90	-	-	90	µs
Turn-On, Vin = 0 to 270 V	Delay	-	15	25	-	15	25	ms
	Overshoot	-	0	50	-	0	50	mVpk
Input Attenuation ³	V _{OUT} / V _{IN} @ 1.2 kHz	-	80	-	-	80	-	dB
FUNCTION								
INH Pin Input ³	Output Inhibited	0	-	1.5	0	-	1.5	V
INH Pin Open Circuit Voltage ³	Output Enabled	-	9	13	-	9	13	V
Voltage Trim Range		-20	-	10	-20	-	10	%
SYNC Frequency Range		500	-	600	500	-	600	kHz
Overvoltage Trip Point ³		13.9	15	16.2	17	18.5	20	V
GENERAL								
Efficiency		85	89	-	85	89	-	%
Capacitive Load ³		-	-	5000	-	-	5000	µF
Switching Frequency	Input	225	250	275	225	250	275	kHz
	Output	450	500	550	450	500	550	kHz
Isolation	2000 V DC, Tcase = 25 °C	100	-	-	100	-	-	MΩ
Weight		-	-	88	-	-	88	g
MTBF (MIL-HDBK-217F)	GM @ Tcase = 55 °C	-	402	-	-	402	-	kHr

1. Time for output voltage to settle within 1% of steady-state value.

2. Derate linearly to 0 at 115 °C.

3. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

4. With additional 1µF ceramic capacitor from VOUT to OUTCOM.

3.2 PERFORMANCE SPECIFICATIONS (CONTINUED)

Tcase = -55 °C to +105 °C, Vin = +270 V ± 5%, Full Load, Unless Otherwise Specified

VXR125-27028S					
Parameter	Conditions	Min	Typ	Max	Units
INPUT					
Voltage	Continuous	160	270	400	V
	Transient, 1 sec ³	125	-	500	V
Current	INH < 0.2 V	-	2	5	mA
	No Load	-	45	85	mA
Ripple Current	20 Hz to 10 MHz	-	110	250	mApp
Undervoltage Lockout	Turn-On	-	-	159.9	V
	Turn-Off ³	-	-	124.9	V
OUTPUT STATIC					
Voltage	+Vout, Tcase = 25 °C	27.58	28	28.42	V
	+Vout, Tcase = -55 °C to +105 °C	27.3	28	28.7	V
Power ²		0	-	125	W
Current ²		0	-	4.46	A
Ripple Voltage ⁴	20 Hz to 10 MHz	-	75	150	mVpp
Line Regulation	VIN = 160 V to 400 V	-	1	20	mV
Load Regulation	No Load to Full Load	-	10	30	mV
Load Fault Power Dissipation	Overload ³	-	19	30	W
	Short Circuit ³	-	17	30	W
OUTPUT DYNAMIC					
Load Step, Half to Full Load	Output Transient	-	200	400	mVpk
	Recovery ¹	-	-	70	μs
Line Step ³ , Vin = 160 V to 400 V	Output Transient	-	150	300	mVpk
	Recovery ¹	-	-	70	μs
Turn-On, Vin = 0 to 270 V	Delay	-	15	25	ms
	Overshoot	-	0	50	mVpk
Input Attenuation ³	VOUT / VIN @ 1.2 kHz	-	80	-	dB
FUNCTION					
INH Pin Input ³	Output Inhibited	0	-	1.5	V
INH Pin Open Circuit Voltage ³	Output Enabled	-	9	13	V
Voltage Trim Range		-20	-	10	%
SYNC Frequency Range		500	-	600	kHz
Overvoltage Trip Point ³		32	35	37.5	V
GENERAL					
Efficiency		85	89	-	%
Capacitive Load ³		-	-	1500	μF
Switching Frequency	Input	225	250	275	kHz
	Output	450	500	550	kHz
Isolation	2000 V DC, Tcase = 25 °C	100	-	-	MΩ
Weight		-	-	88	g
MTBF (MIL-HDBK-217F)	GM @ Tcase = 55 °C	-	402	-	kHr

1. Time for output voltage to settle within 1% of steady-state value.

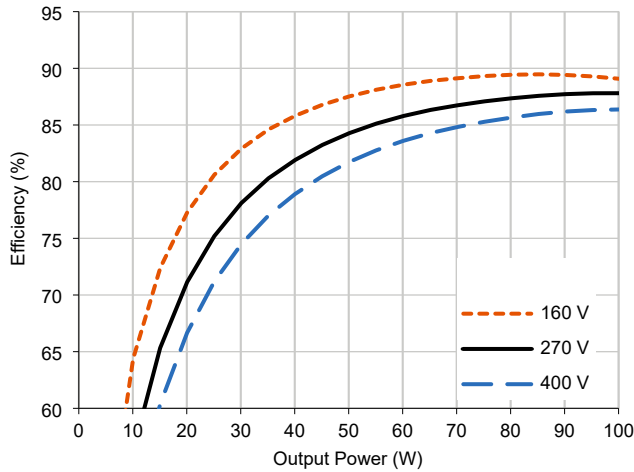
2. Derate linearly to 0 at 115 °C.

3. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.

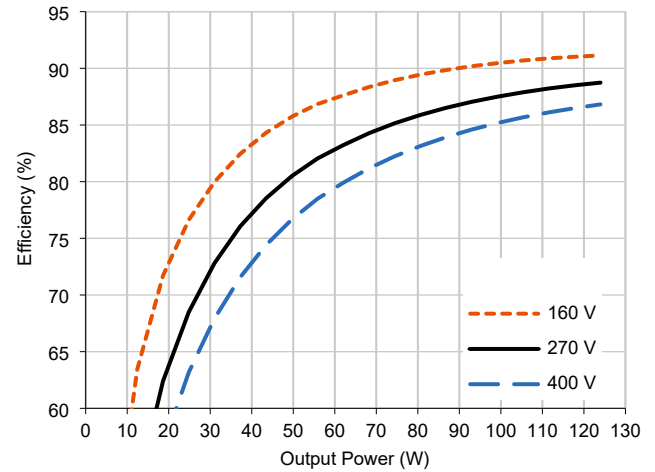
4. With additional 1μF ceramic capacitor from VOUT to OUTCOM.

4.0 PERFORMANCE CURVES

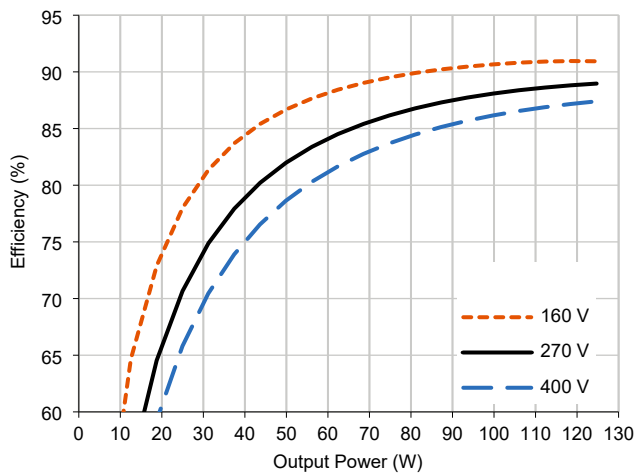
4.1.1 VXR125-2703R3S Efficiency (Typical, 25 °C)



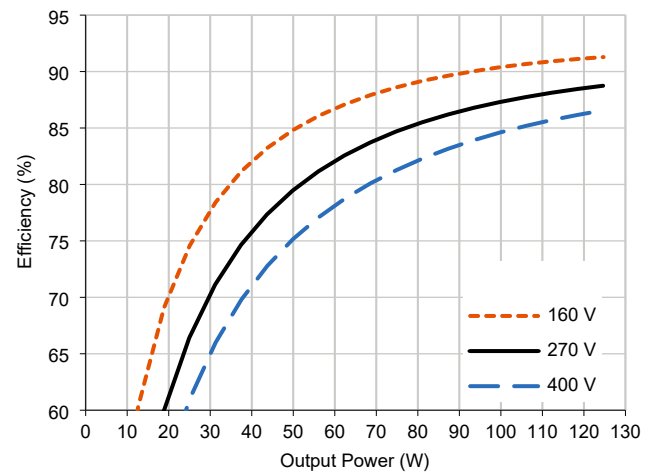
4.1.2 VXR125-27005S Efficiency (Typical, 25 °C)



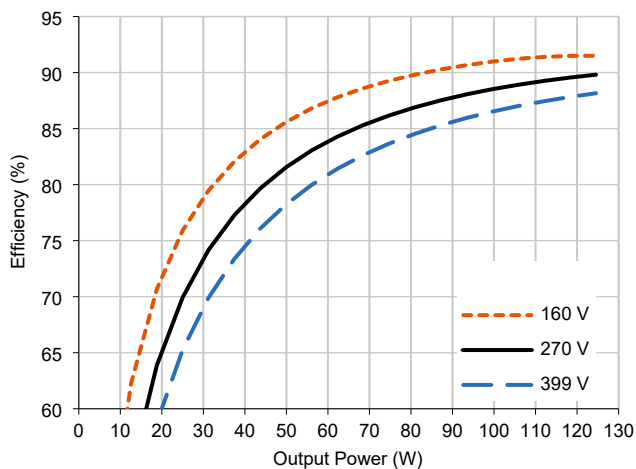
4.1.3 VXR125-27012S Efficiency (Typical, 25 °C)



4.1.4 VXR125-27015S Efficiency (Typical, 25 °C)

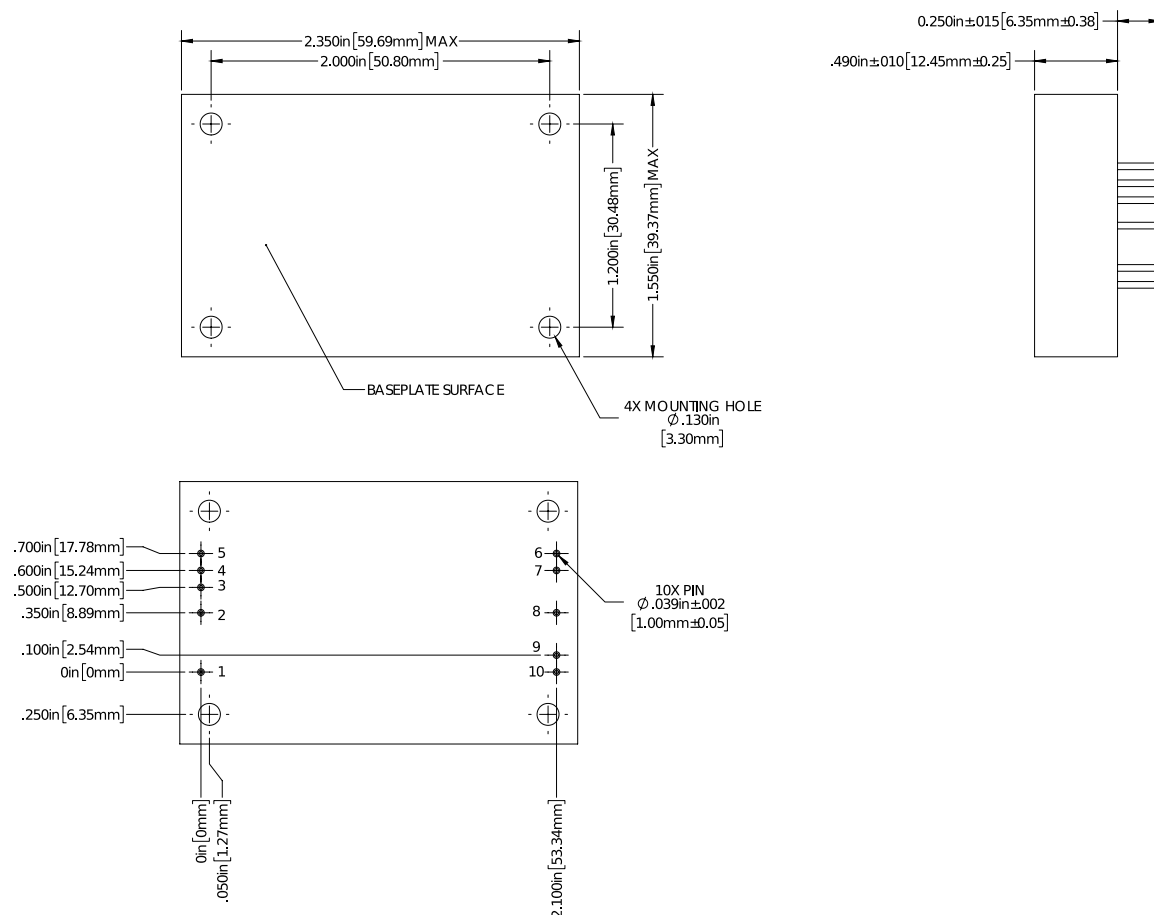


4.1.5 VXR125-27028S Efficiency (Typical, 25 °C)



5.0 MECHANICAL OUTLINES AND PINOUT

Standard Package:



1. Tolerances are ± 0.010 " unless otherwise stated.
2. Case temperature is measured on the center of the baseplate surface.
3. Mounting holes are not threaded. Recommended fastener is 4-40.
4. Materials: Body (Epoxy with integral metalized EMI shield); Pin (Tellurim Copper, alloy 145, gold over nickel plating).

Pin	Function	Pin	Function	Pin	Function	Pin	Function
1	270VIN	4	INCOM	7	-SENSE	10	+VOUT
2	CASE	5	SYNC	8	TRIM		
3	INH	6	OUTCOM	9	+SENSE		

6.0 TECHNICAL NOTES

Please note that many of these functions are also demonstrated in detail on the VPT website in the form of technical video labs.

6.1 GENERAL INFORMATION

6.1.1 Topology Description

The VXR125-27000S Series features a two-stage power conversion architecture. The first power stage is a zero-voltage-switched buck converter, followed by a synchronous rectified, symmetrically controlled half-bridge topology as the second stage. The design delivers high efficiency across a wide input voltage range and provides excellent attenuation to input disturbances. Proprietary secondary-side feedback control ensures a tightly regulated positive output voltage with fast transient response, eliminating the need for analog feedback isolation.

6.1.2 External Components

The VXR125-27000S Series is designed to operate stand-alone and does not require external components for proper operation. An additional 1 μ F capacitor from +VOUT to OUTCOM reduces the output voltage ripple and is required to meet datasheet specifications. Input and output L-C filters are provided internally for low ripple and noise. To further reduce output ripple and noise for more sensitive applications, a small ceramic capacitor, 1 μ F to 10 μ F, can be added to the output. Most application specific ripple requirements can be met with the addition of output capacitors alone. External output capacitance can be added up to the maximum listed in Section 3.2.

6.1.3 Source Impedance

The impedance of the 270 V input source can interact with the DC-DC converter and may affect performance. High source impedance is often caused by a long input cable or components added in series with the input. Source resistance will cause a DC voltage drop as the converter draws DC input current. This voltage drop is determined by multiplying the cable resistance by the input current at low line. The voltage drop and the actual voltage at the input to the converter will determine the minimum source voltage at which the converter will operate. A high source inductance can interact with the feedback control loop of the converter. VPT's EMI filters will typically isolate the source and eliminate this problem. In some cases, additional input capacitance may be needed to stabilize the system.

6.1.4 Output Configurations

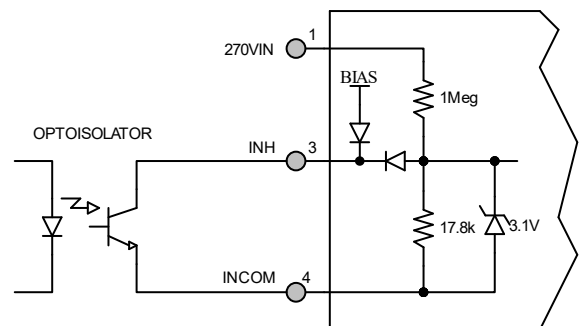
Since the converter is isolated, the output can be configured as a positive or negative voltage. For example, the VXR125-27012S can provide +12 V DC referenced to OUTCOM or -12 V DC referenced to +VOUT.

The outputs of multiple converters can be stacked in series to provide higher voltages. When outputs of multiple modules are stacked, they naturally share the load. For example, two VXR125-27012S converters can be stacked to provide a 24 V DC output at 250 W.

6.2 FUNCTION DESCRIPTIONS

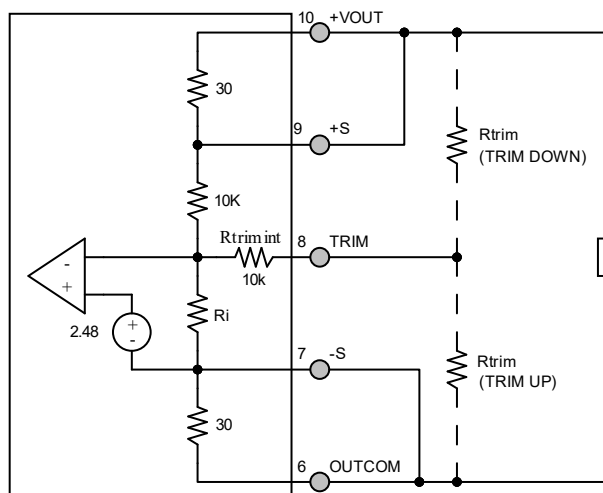
6.2.1 On/Off Control (Inhibit)

The INH (Inhibit) pin is a primary-side control pin referenced to INCOM. The INH pin must be driven using an open collector or open drain configuration. Pulling the INH pin low disables the converter output, removes bias voltage from internal control circuitry, and puts the converter in a state of minimum input current draw. Leaving INH open enables the output, allowing the converter to operate normally. The pin must be pulled below 1.5 V to disable the output. The INH pin should be left open if not used.



6.2.2 Adjusting the Output Voltage (TRIM)

The output voltage set point of the converter can be adjusted using the TRIM pin. To adjust the output up, connect the trim resistor from TRIM to OUTCOM. To adjust the output down, connect the trim resistor from TRIM to +VOUT. The maximum trim range is -20% and +10% from nominal. The appropriate resistor values versus the output voltage are given in the table below. Because the TRIM pin can be sensitive to external noise, the trim resistor should be physically located close to the VXR125 converter with short interconnects. The TRIM pin should be left open if not used. When adjusting the output voltage, do not exceed the rated total output power or output current.



VXR125-2703R3S		VXR125-27005S		VXR125-27012S		VXR125-27015S		VXR125-27028S	
+Vout (V)	Rtrim (Ω)	+ Vout (V)	Rtrim (Ω)	+ Vout (V)	Rtrim (Ω)	+ Vout (V)	Rtrim (Ω)	+ Vout (V)	Rtrim (Ω)
3.6	72.7k	5.5	39.7k	13.2	10.7k	16.5	6.5k	30.5	10k
3.55	89.2k	5.4	52.1k	13	14.8k	16.25	9.8k	30	15k
3.5	114.0k	5.3	72.9k	12.8	21.0k	16	14.8k	29.5	23.5k
3.45	155.4k	5.2	114.6k	12.6	31.3k	15.75	23.1k	29	40.5k
3.4	238k	5.1	240k	12.4	51.9k	15.5	39.6k	28.5	92.8k
3.35	487k	5		12.2	113.7k	15.25	89.1k	28	
3.3		4.9	230k	12		15		27.5	957k
3.25	144k	4.8	105k	11.8	457k	14.75	482k	27	472k
3.2	61.9k	4.7	64.0k	11.6	218k	14.5	231k	26.5	307k
3.15	34.7k	4.6	42.9k	11.4	139k	14.25	147k	26	223k
3.1	21.0k	4.5	30.3k	11.2	99.1k	14	105k	25.5	173k
3.05	12.8k	4.4	22.0k	11	75.2k	13.75	80.2k	25	140k
3	7.33k	4.3	16.0k	10.8	59.4k	13.5	63.5k	24.5	115k
2.95	3.43k	4.2	11.5k	10.6	48.0k	13.25	51.6k	24	97.1k
2.9	498	4.1	8.0k	10.4	39.5k	13	42.6k	23.5	83.1k
		4	5.2k	10.2	32.9k	12.75	35.7k	23	71.8k
				10	27.6k	12.5	30.1k	22.5	62.6k
				9.8	23.3k	12.25	25.5k		
				9.6	19.7k	12	21.7k		

6.2.3 Remote Sense (+S, -S)

Remote sensing compensates for voltage drops in the output pins and traces by sensing and regulating directly at the load. The remote sense function can compensate for up to a 10% voltage drop (5% in the positive lead and 5% in the negative lead). The sum of the TRIM up and remote sense compensation cannot exceed 10% such that the voltage at the output terminals of the converter does not exceed 110% of the nominal output voltage. If not used, the user should connect +S to +VOUT and -S to OUTCOM at the pins of the converter.

6.2.4 Frequency Synchronization (SYNC)

The VXR125-27000S Series will free-run at a switching frequency of approximately 500 kHz seen at the output and 250 kHz seen at the input, which has been set for optimum converter performance. The divide-by-two function is set internally between the two power stages that are synchronized to an internal 500 kHz clock. Frequency synchronization is not necessary unless required by system constraints. The VXR125-27000S Series provides a frequency synchronization input (SYNC) referenced to INCOM.

The SYNC pin can be driven by an external clock. The internal clock and internal power train will operate at the frequency applied to the SYNC pin. The SYNC pin should be driven with a TTL type 5 V square wave signal. The duty cycle of the square wave should be between 40% and 60%. Proper layout and circuit techniques are necessary to prevent noise from being injected into this pin. Synchronized converters should be located physically close together and share a low impedance INCOM connection. The SYNC pin should be connected to INCOM if not used.

6.3 PROTECTION FEATURES

6.3.1 Input Undervoltage Lockout

The VXR125-27000S Series provides input undervoltage lockout protection. For input voltages below the turn-on voltage, the converter will remain off, drawing minimal current from the source. When the input voltage exceeds the turn-on voltage, the converter will start. The lockout circuit is designed to accept slow ramping input voltage waveforms. The VXR125-27000S circuit provides bias voltage to all secondary control circuits and control amplifiers before the output starts, ensuring a well-controlled start up sequence.

6.3.2 Output Soft Start

The VXR125-27000S Series utilizes an output soft-start function to ramp the output in a controlled manner, eliminating output voltage overshoot and limiting inrush current at turn on. A voltage-mode soft-start ensures the output waveform remains consistent regardless of changes in the load current. The output rise time is approximately 10ms. The soft-start function is active whether the module is turned on with an application of input voltage or from release of the inhibit pin. Under normal conditions, current drawn from the source during turn-on will not exceed the full-load input current. The turn-on delay time, which is approximately 15ms, is specified from the application of input voltage (or release of the inhibit pin) until the output reaches 90% of its final value.

6.3.3 Output Overcurrent Protection

The VXR125-27000S Series provides output overcurrent and output short circuit protection. During a load fault condition, a constant output current control circuit reduces the converter duty cycle to limit the output current to approximately 130% of its rated value. The converter will continue to provide constant current into any overload or short circuit condition. This feature allows the converter to start into any capacitive load. Recovery is automatic and immediate upon removal of the fault condition. Sustained short circuit or overload operation can cause excessive power dissipation. Care should be taken to control the operating temperature of the converter in this condition.

6.3.4 Output Overvoltage Protection

The VXR125-27000S Series provides output overvoltage protection. During an output over voltage fault condition, the converter will terminate all switching on the second stage until the fault is cleared. Once the fault is cleared, the converter will begin to soft-start back to its programmed output voltage.

6.4 PACKAGE

6.4.1 Package Description

The VXR125-27000S is constructed with an epoxy encapsulated V-SHIELD® package that includes integral metallization for enhanced EMI shielding. The ruggedized package is highly resistant to chemical, solvent and salt environments and is fully compatible with high volume manufacturing processes including wave solder, cleaning solvents, high pressure sprays and aqueous wash processes. The encapsulated converter allows for superior shock and vibration performance in a low-weight package.

6.4.2 Thermal Performance

The VXR125-27000S Series is rated for full power operation at 105 °C. Operation above 105 °C is allowed at reduced power. Specifically, the output power should be derated linearly from full power at 105 °C to half power at 110 °C and to zero power at 115 °C. The operating temperature of the converter is specified on the baseplate or top plate of the converter. The converter is designed to be conduction-cooled, with either the baseplate or top plate mounted to a heat sink, chassis, PCB or other thermal surface.

The DC-DC converter contains many semiconductor components. The maximum temperature rise from junction to case is 23 °C at full load.

7.0 ENVIRONMENTAL SCREENING

Test	Condition
Internal Visual	IPC-A-610, Class 3
Temperature Cycling	MIL-STD-883, Method 1010, Condition B, -55 °C to +125 °C, 10 Cycles
Burn In	96 hours at +105 °C
Final Electrical	100% at 25 °C
External Visual	Internal Procedure

8.0 ORDERING INFORMATION

VXR125-	270	05	S
1	2	3	4

(1) Product Series	(2) Nominal Input Voltage	(3) Output Voltage	(4) Number of Outputs
VXR125-	270 270 Volts	3R3 3.3 Volts 05 05 Volts 12 12 Volts 15 15 Volts 28 28 Volts	S Single

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirements, and source inspection.

9.0 CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT, Inc. Sales Department at:

Phone: (425) 353-3010
Fax: (425) 353-4030
E-mail: vptsales@vptpower.com

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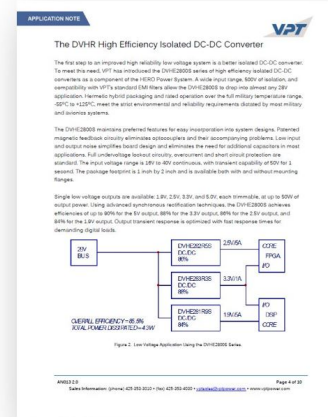
10.0 ADDITIONAL INFORMATION

Visit the VPT website for additional technical resources, including:

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