

NB7141ZA Series

1-cell Li-ion Battery Protection IC with Forced Standby and Built-In Watchdog Timer

FEATURES

- Supply Current
 - Normal mode: Typ. 2.00 μ A / Max. 4.00 μ A
 - Forced standby mode: Max. 0.04 μ A
 - Standby mode: Max. 0.20 μ A
 - (V_{DET2}: Auto release type)
 - Max. 0.04 μ A
 - (V_{DET2}: Latch type)
- Detector Selectable Range and Accuracy
 - Overcharge detection voltage (V_{DET1}): 4.2 V to 4.7 V, \pm 15 mV
 - Overdischarge detection voltage (V_{DET2}): 2.1 V to 3.2 V, \pm 35 mV
 - Discharge overcurrent detection voltage 1 (V_{DET31}): 0.0050 V to 0.0300 V, \pm 1.0 mV
0.0305 V to 0.0500 V, \pm 1.5 mV
 - Discharge overcurrent detection voltage 2 (V_{DET32}): 0.0110 V to 0.0600 V, \pm 2 mV
0.0605 V to 0.1000 V, \pm 4.0 %
 - Charge overcurrent detection voltage (V_{DET4}): -0.0050 V to -0.0300 V, \pm 1.0 mV
-0.0305 V to -0.0500 V, \pm 1.5 mV
 - Short-circuit detection voltage 1 (V_{SHORT1}): 0.030 V to 0.120 V, \pm 4.0 mV
0.121 V to 0.200 V, \pm 5.0 mV
- 0 V Battery Charging selectable: Permission / Inhibition
- 0 V Battery Charging Inhibition Voltage (V_{NOCHG}): 1.000 V to 2.500 V, \pm 4.0 %
- Overcharge Release Type selectable: Auto Release / Latch
- Overdischarge Release Type selectable: Auto Release / Latch
- Discharge Overcurrent Release Type selectable: Auto Release1 (V₋ = V_{DD} \times 0.8 V) / Auto Release2 (V₋ = 0.070 V) / Latch (At charger connection)
- Discharge Overcurrent Detection Voltage 2 (V_{DET32}) selectable: Available / Unavailable
- WDT Release Type selectable: Auto Release / V₋ rising

APPLICATIONS

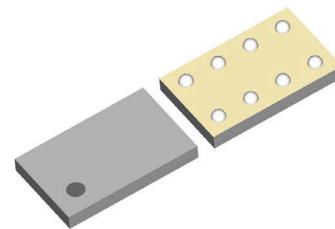
Hearable / Wearable devices,
Smart Phone,
Handheld Data Terminals

GENERAL DESCRIPTION

The NB7141ZA is a 1-cell Li-ion / polymer battery protection IC which provides overcharge, over-discharge, charge / discharge overcurrent and short circuit protection, with built-in watchdog timer. The NB7141ZA can shift to the Forced Standby Mode by an external signal to STB pin to reduce own current consumption.

Low voltage and high accuracy overcurrent detection achieves a heat reduction on board by using low sense resistor. Low operating current and standby current can prolong the battery life even when its capacity is small, and the forced standby mode with STB pin too can prolong the small battery life.

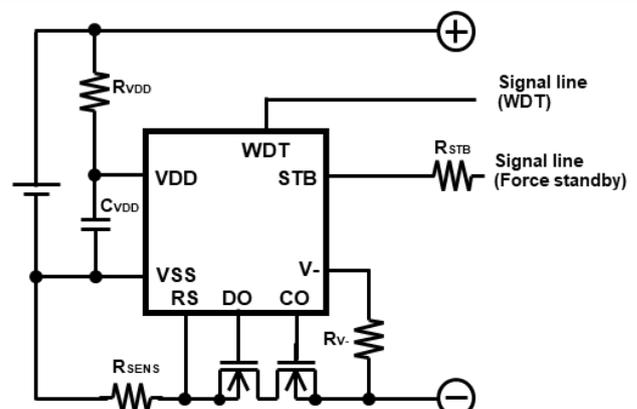
The built-in watchdog timer (WDT) can monitor the MCU operating condition.



WLCSP-8-P10

1.6 \times 1.0 \times 0.3 [mm]

TYPICAL APPLICATION CIRCUIT



PRODUCT NAME INFORMATION

NB7141 ZA *** * * E2 S
 aa bbb c d ee f

Description of configuration

| Suffix | Item | Description |
|--------|----------------------|--|
| aa | Package code | Indicates the package code. |
| bbb | Specific option code | Indicates a three-digit number code that combined set voltages. Refer to the table of set voltages for details. |
| c | | Indicates a delay time code. Refer to the table of delay times for details. |
| d | | Indicates a function code. Refer to the table of functions for details. |
| ee | Packing | Indicates the taping code of the package. Refer to <i>Packing Specification</i> in the appendix <i>Package Information</i> for details. |
| f | Grade | Indicates the quality grade. Refer to the table of grade for details. |

Table of set voltages (bbb)

| Symbol | V _{DET1} | V _{REL1} ^{*1} | V _{DET2} | V _{REL2} ^{*1} | V _{DET31} ^{*2} | V _{DET32} ^{*2} | V _{DET4} | V _{SHORT1} ^{*2} | V _{NOCHG} |
|--------------------------------|--------------------------|---------------------------------|--------------------------|---------------------------------|----------------------------------|----------------------------------|-----------------------------------|-----------------------------------|-----------------------------|
| Voltage Range (Step) [V] | 4.2 to 4.7 (0.005) | 4.0 to 4.7 (0.005) | 2.1 to 3.2 (0.005) | 2.3 to 3.6 (0.005) | 0.0050 to 0.0500 (0.0005) | 0.0110 to 0.1000 (0.0005) | -0.0050 to -0.0500 (0.0005) | 0.030 to 0.200 (0.001) | 1.000 to 2.500 (0.05) |

^{*1} Under the following conditions,

V_{REL1}: V_{DET1} - V_{REL1} = 0.400 V (Max.)

V_{REL2}: V_{REL2} - V_{DET2} = 0.700 V (Max.)

^{*2} When selecting each set voltage of V_{DET31}, V_{DET32} and V_{SHORT1}, keep from overlapping among them in consideration of their output voltage accuracy. Especially, V_{SHORT1} should be higher than 10 mV from V_{DET31} and V_{DET32}.

Table of delay times (c)

| Symbol | t _{VDET1} | t _{VREL1} | t _{VDET2} | t _{VREL2} | t _{VDET31} ^{*1} | | t _{VDET32} | t _{VREL3} | t _{VDET4} | t _{VREL4} | t _{SHORT} | t _{STBD} | t _{STBR} | t _{WD} | t _{WR} |
|--------------|--------------------|--------------------|--------------------|--------------------|--------------------------------------|----------------------------------|---------------------|--------------------|--------------------|--------------------|--------------------|-------------------|--------------------|--------------------------------|-----------------|
| | | | | | type A | type B | | | | | | | | | |
| Time [ms] | 1024 | 16 | 32 64 128 | 1.05 | 1024.0 2048.0 3072.0 4096.0 | 12.5 16.5 1024.0 4096.0 | 7.5 12.5 16.5 | 9.0 | 10 17 | 4 | 0.28 | 50 300 | 4.5 8.5 33.0 | 1024 4096 10240 25600 | 200 1024 |
| C | 1024 | 16 | 128 | 1.05 | 4096.0 | | - | 9.0 | 10 | 4 | 0.28 | 300 | 33.0 | 25600 | 200 |
| E | 1024 | 16 | 128 | 1.05 | 4096.0 | | - | 9.0 | 10 | 4 | 0.28 | 300 | 33.0 | 25600 | 1024 |
| H | 1024 | 16 | 32 | 1.05 | 1024.0 | | - | 9.0 | 17 | 4 | 0.28 | 50 | 4.5 | 1024 | 200 |

^{*1} t_{VDET31} can be selected from two types, A and B, with mask options, and each type has four patterns with trimming options.

Table of functions (d)

| Function | Overcharge Release | Overdischarge Release *1 | Discharge Overcurrent Release | WDT Reset Release | Type of t_{VDET31} | Discharge Overcurrent Detection 2 (V_{DET32}) | 0 V Battery Charging | V_{STBD} [V] | V_{WDT} [V] |
|----------------|--------------------|--------------------------|--------------------------------------|---------------------------|----------------------|---|--------------------------|------------------------------|---------------|
| Type/Condition | Auto Release Latch | Auto Release Latch | Auto Release1 Auto Release2 Latch | V- Rising Auto Release | A B | Available Unavailable | Permission Inhibition | 0.65 0.80 1.20 1.80 | 0.65 |
| H | Latch | Latch | Auto Release2 | Auto Release | A | Unavailable | Inhibition | 1.20 | 0.65 |
| M | Latch | Latch | Auto Release1 | Auto Release | B | Unavailable | Inhibition | 0.65 | 0.65 |
| N | Latch | Latch | Auto Release1 | Auto Release | B | Unavailable | Permission | 0.65 | 0.65 |
| R | Auto Release | Auto Release | Auto Release1 | Auto Release | B | Unavailable | Inhibition | 0.65 | 0.65 |

*1 Overdischarge Release Conditions,
 Auto Release: Cell voltage > V_{REL2}
 Latch: Cell voltage > V_{DET2} under charger connection

Grade

| Grade | Application | Operating Temperature Range | Test Temperature |
|-------|------------------------------|-----------------------------|------------------|
| S | General-purpose and Consumer | -40°C to 85°C | 25°C |

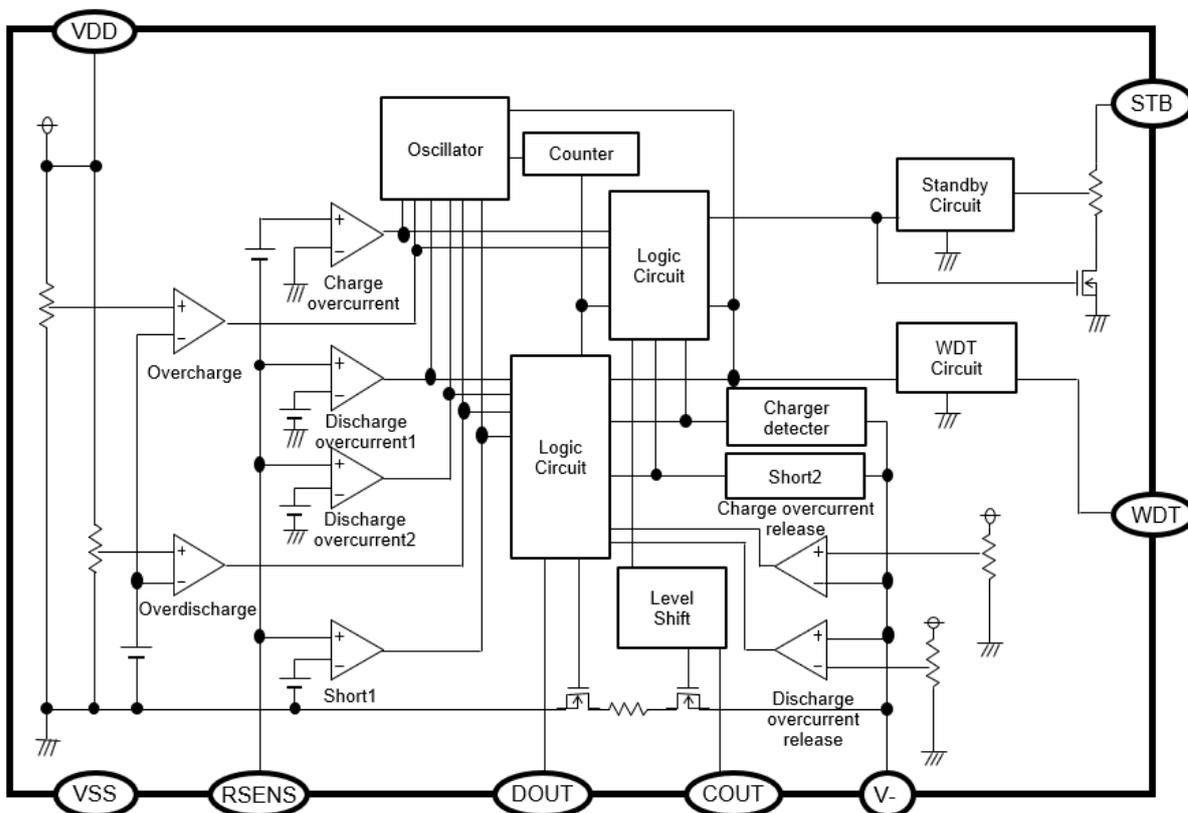
ORDER INFORMATION

For details of the orderable products, please refer to the appendix "Product Code List".

| Product Name | Package | RoHS | Halogen-Free | Plating Composition | Weight [mg] | Quantity Per Reel [pcs] |
|------------------|-------------|------|--------------|---------------------|-------------|-------------------------|
| NB7141ZA*****E2S | WLCSP-8-P10 | Yes | Yes | Sn3.0Ag0.5Cu | 0.73 | 5,000 |

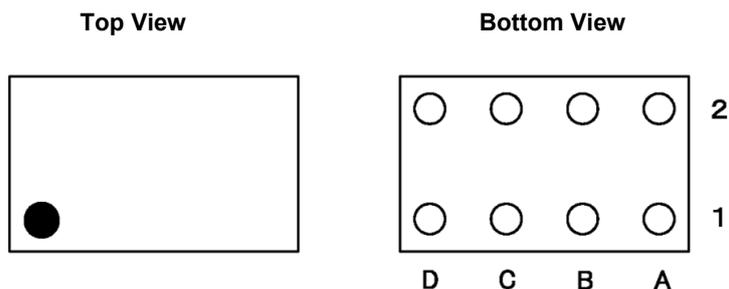
Note: Contact our sales representatives for other specific option code (indicated with five asterisks).

BLOCK DIAGRAM



NB7141ZA Block Diagram

PIN DESCRIPTION



NB7141ZA (WLCSP-8-P10) Pin Configuration

| Pin No. | Pin Name | I/O | Description |
|---------|----------|-----|---|
| A1 | STB | I | Forced standby instruction signal input pin |
| B1 | V- | I | Charger negative input pin |
| C1 | COUT | O | Charge control pin, CMOS output |
| D1 | DOUT | O | Discharge control pin, CMOS output |
| A2 | WDT | I | Watchdog timer signal input pin |
| B2 | RSENS | I | Overcurrent detection input pin |
| C2 | VDD | - | Power supply pin, the substrate level of the IC |
| D2 | VSS | - | Ground pin for the IC |

ABSOLUTE MAXIMUM RATINGS

| Parameter | Symbol | Rating | Unit |
|----------------------------|-------------|----------------------------------|------|
| Supply voltage | V_{DD} | -0.3 to 12 | V |
| V- pin input voltage | V- | $V_{DD} - 30$ to $V_{DD} + 0.3$ | V |
| RSENS pin input voltage | V_{RSENS} | $V_{SS} - 0.3$ to $V_{DD} + 0.3$ | V |
| WDT pin input voltage | V_{WDT} | $V_{DD} - 30$ to $V_{DD} + 0.3$ | V |
| STB pin input voltage | V_{STB} | $V_{DD} - 30$ to $V_{DD} + 0.3$ | V |
| COOUT pin output voltage | V_{COOUT} | $V_{DD} - 30$ to $V_{DD} + 0.3$ | V |
| DOOUT pin output voltage | V_{DOOUT} | $V_{SS} - 0.3$ to $V_{DD} + 0.3$ | V |
| Power Dissipation | P_D | 150 | mW |
| Junction Temperature Range | T_j | -40 to 125 | °C |
| Storage Temperature Range | T_{stg} | -55 to 125 | °C |

ABSOLUTE MAXIMUM RATINGS

Electronic and mechanical stress momentarily exceeded absolute maximum ratings may cause permanent damage and may degrade the lifetime and safety for both device and system using the device in the field. The functional operation at or over these absolute maximum ratings is not assured.

ELECTROSTATIC DISCHARGE RATINGS

| Parameter | Conditions | Rating | Unit |
|----------------------------|------------------------------------|------------|------|
| HBM (Human Body Model) | $C = 100$ pF, $R = 1.5$ k Ω | ± 2000 | V |
| CDM (Charged Device Model) | Field Included CDM (FI-CDM) | ± 1000 | V |

ELECTROSTATIC DISCHARGE RATINGS

The electrostatic discharge test is done based on JESD47.
In the HBM method, ESD is applied using the power supply pin and GND pin as reference pins.

RECOMMENDED OPERATING CONDITIONS

| Parameter | Symbol | Rating | Unit |
|-----------------------------|----------|------------|------|
| Operating input voltage | V_{DD} | 1.5 to 5.0 | V |
| Operating temperature range | T_a | -40 to 85 | °C |

RECOMMENDED OPERATING CONDITIONS

All of electronic equipment should be designed that the mounted semiconductor devices operate within the recommended operating conditions. The semiconductor devices cannot operate normally over the recommended operating conditions, even if they are used over such conditions by momentary electronic noise or surge. And the semiconductor devices may receive serious damage when they continue to operate over the recommended operating conditions.

ELECTRICAL CHARACTERISTICS

Ta = 25°C, unless otherwise noted.

NB7141ZA**E2S Electrical Characteristics**

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Remark ^{*1} |
|--|---------------------|---|-------------------------------|------------------------|--|------|----------------------|
| Minimum charging voltage for 0 V battery charger ^{*2} | V _{STCHG} | V _{DD} - V _{SS} = 0 V, defined as V _{DD} - V ₋ difference voltage | | | 1.8 | V | B |
| 0 V battery charging inhibition voltage ^{*3} | V _{NOCHG} | V _{DD} - V ₋ = 4 V, defined as V _{DD} - V _{SS} difference voltage | V _{NOCHG} × 0.96 | V _{NOCHG} | V _{NOCHG} × 1.04 | V | A |
| Overcharge detection voltage | V _{DET1} | R _{VDD} = 330Ω, C _{VDD} = 0.1μF | V _{DET1} - 0.015 | V _{DET1} | V _{DET1} + 0.015 | V | A |
| Overcharge release voltage | V _{REL1} | R _{VDD} = 330Ω, C _{VDD} = 0.1μF | V _{REL1} - 0.045 | V _{REL1} | V _{REL1} + 0.045 | V | A |
| Overcharge detection delay time | t _{VDET1} | V _{DD} = 3.6 V → V _{DET1} + 0.1 V | t _{VDET1} × 0.80 | t _{VDET1} | t _{VDET1} × 1.20 | ms | A |
| Overcharge release delay time | t _{VREL1} | Overcharge: Auto Release V _{DD} = 4.8 V → V _{REL1} - 0.1 V | 12.8 | 16.0 | 19.2 | ms | A |
| | | Overcharge: Latch V _{DD} = 4.8 V → V _{DET1} - 0.1 V V ₋ = 0.2 V | | | | | |
| Overdischarge detection voltage | V _{DET2} | Detect falling edge of supply voltage | V _{DET2} - 0.035 | V _{DET2} | V _{DET2} + 0.035 | V | C |
| Overdischarge release voltage | V _{REL2} | Detect rising edge of supply voltage | V _{REL2} - 0.055 | V _{REL2} | V _{REL2} + 0.095 | V | C |
| Overdischarge detection delay time | t _{VDET2} | V _{DD} = V _{DET2} + 0.15 V → V _{DET2} - 0.10 V | t _{VDET2} × 0.80 | t _{VDET2} | t _{VDET2} × 1.20 | ms | C |
| Overdischarge release delay time | t _{VREL2} | Overdischarge: Auto Release V _{DD} = V _{DET2} - 0.04 V → V _{REL2} + 0.1 V | 0.75 | 1.05 | 1.53 | ms | C |
| | | Overdischarge: Latch V _{DD} = V _{DET2} - 0.04 V → V _{DET2} + 0.04 V, V ₋ = 0.0 V | | | | | |
| V ₋ pin pullup resistance for V _{DET2} | R _{V-1} | V _{DD} = 1.8 V | 0.1 | 0.6 | 1.0 | MΩ | E |
| Discharge overcurrent detection voltage 1 | V _{DET31} | V _{DD} = 3.6 V, Detect rising edge of V _{RSENS} | V _{DET31} ≤ 0.030 V | V _{DET31} | V _{DET31} + 0.0010 V _{DET31} + 0.0015 | V | F |
| | | | V _{DET31} > 0.030 V | | | | |
| Discharge overcurrent detection delay time 1 | t _{VDET31} | V _{DD} = 3.6 V, V _{RSENS} = 0.0 V → V _{DET31} + 0.005 V | t _{VDET31} × 0.80 | t _{VDET31} | t _{VDET31} × 1.20 | ms | F |
| Discharge overcurrent detection voltage 2 | V _{DET32} | V _{DD} = 3.6 V, Detect rising edge of V _{RSENS} | V _{DET32} ≤ 0.060 V | V _{DET32} | V _{DET32} + 0.002 V _{DET32} × 1.04 | V | F |
| | | | V _{DET32} > 0.060 V | | | | |
| Discharge overcurrent detection delay time 2 | t _{VDET32} | V _{DD} = 3.6 V, V _{RSENS} = 0.0 V → V _{DET32} + 0.005 V | t _{VDET32} × 0.80 | t _{VDET32} | t _{VDET32} × 1.20 | ms | F |
| Short circuit detection voltage 1 | V _{SHORT1} | V _{DD} = 3.6 V, Detect rising edge of V _{RSENS} | V _{SHORT1} ≤ 0.120 V | V _{SHORT1} | V _{SHORT1} + 0.004 V _{SHORT1} + 0.005 | V | F |
| | | | V _{SHORT1} > 0.120 V | | | | |
| Short circuit detection delay time ^{*4} | t _{SHORT} | V _{DD} = 3.6 V, V _{RSENS} = 0.0 V → 1.0 V | 210 | 280 | 380 | μs | F |
| Short circuit detection voltage 2 | V _{SHORT2} | Detect rising edge of V ₋ , V _{DD} = 3.6 V, V _{RSENS} = 0.0 V | V _{DD} - 2.00 | V _{DD} - 1.50 | V _{DD} - 0.80 | V | G |

*1 The test circuits for device evaluation. Refer to the section of TEST CIRCUITS for detail information.

*2 Only 0 V battery charging permission type

*3 Only 0 V battery charging inhibition type

*4 Short circuit release delay time is same as t_{VREL3}.

Ta = 25°C, unless otherwise noted

NB7141ZA**E2S Electrical Characteristics (Continued)**

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Remark ^{*1} | |
|---|---------------------|---|------------------------------|---------------------------------|---------------------------|---------------------------------|----------------------|---|
| Charger detection voltage | V _{CHGDET} | Detect falling edge of V ₋ , R _{V-} = 1.0kΩ, V _{DD} = V _{DET2} + 0.05 V | 0.500 | 0.800 | 1.100 | V | G | |
| Discharge overcurrent release voltage | V _{REL3} | Detect falling edge of V ₋ , V _{DD} = 3.6 V, V _{RSENS} = 0.0 V | Auto Release1 | V _{DD} × 0.800 - 0.050 | V _{DD} × 0.800 | V _{DD} × 0.800 + 0.050 | V | G |
| | | | Auto Release2 | 0.040 | 0.070 | 0.100 | | |
| | | | Latch | 0.040 | 0.070 | 0.100 | | |
| Discharge overcurrent release resistance | R _{SHORT} | Auto Release 1: V _{DD} = 3.6 V, V ₋ = V _{REL3} + 0.050 V | 6.5 | 10.0 | 13.5 | kΩ | H | |
| | | Auto Release 2: V _{DD} = 3.6 V, V ₋ = 0.200 V | 20 | 45 | 70 | | | |
| Discharge overcurrent release delay time | t _{VREL3} | V _{DD} = 3.6 V, V ₋ = 3.6 V → 0.0 V, V _{RSENS} = 0.0 V | 7.3 | 9.0 | 10.8 | ms | G | |
| Charge overcurrent detection voltage | V _{DET4} | V _{DD} = 3.6 V, Detect falling edge of V _{RSENS} | V _{DET4} ≥ -0.030 V | V _{DET4} - 0.0010 | V _{DET4} | V _{DET4} + 0.0010 | V | I |
| | | | V _{DET4} < -0.030 V | V _{DET4} - 0.0015 | | V _{DET4} + 0.0015 | | |
| Charge overcurrent detection delay time | t _{VDET4} | V _{DD} = 3.6 V, V _{RSENS} = 0.0 V → -0.5 V | t _{VDET4} × 0.80 | t _{VDET4} | t _{VDET4} × 1.20 | ms | I | |
| Charge overcurrent release voltage | V _{REL4} | Detect rising edge of V ₋ , V _{DD} = 3.6 V, V _{RSENS} = 0.0 V | 0.040 | 0.070 | 0.100 | V | J | |
| Charge overcurrent release delay time | t _{VREL4} | V _{DD} = 3.6 V, V ₋ = -0.5 V → 1.0 V, V _{RSENS} = 0.0 V | 3.2 | 4.0 | 4.8 | ms | J | |
| Forced standby detection voltage | V _{STBD} | Detect rising edge of STB, V _{DD} = 3.6 V, V ₋ = V _{RSENS} = 0.0 V | V _{STBD} × 0.80 | V _{STBD} | V _{STBD} × 1.20 | V | K | |
| Forced standby detection delay time | t _{STBD} | V _{DD} = 3.6 V, V _{STB} = 0.0 V → 3.6 V, V ₋ = V _{RSENS} = 0.0 V | t _{STBD} × 0.80 | t _{STBD} | t _{STBD} × 1.20 | ms | K | |
| Forced standby release delay time | t _{STBR} | V _{DD} = 3.6 V, V ₋ = 3.6 V → 0.0 V, V _{STB} = V _{RSENS} = 0.0 V | t _{STBR} × 0.80 | t _{STBR} | t _{STBR} × 1.20 | ms | L | |
| STB pin pulldown resistance | R _{STBPD} | V _{DD} = 3.6 V, V _{STB} = 3.6 V, V ₋ = V _{RSENS} = 0 V | 5.5 | 11.0 | 22.0 | MΩ | M | |
| V ₋ pin pullup resistance in forced standby mode | R _{V-2} | V _{DD} = 3.6 V, V _{STB} = 3.6 V, V ₋ = V _{RSENS} = 0.0 V | 40 | 70 | 120 | kΩ | N | |
| WDT pin detection voltage, high / low | V _{WDT} | V _{DD} = 3.6 V, V ₋ = V _{RSENS} = 0 V | 0.45 | 0.65 | 0.85 | V | O | |
| Watchdog timeout period | t _{WD} | V _{DD} = 3.6 V, V ₋ = V _{RSENS} = 0 V | t _{WD} × 0.80 | t _{WD} | t _{WD} × 1.20 | ms | O | |
| Watchdog timer reset hold time | t _{WR} | Auto Release Type: V _{DD} = 3.6 V | t _{WR} × 0.80 | t _{WR} | t _{WR} × 1.20 | ms | O, P | |
| | | V ₋ Rising Type: V _{DD} = 3.6 V, V ₋ = 0.0 V → 3.6 V | | | | | | |
| WDT pin input pulse width, high | t _{WDH} | V _{DD} = 3.6 V, V ₋ = V _{RSENS} = 0 V | 0.5 | | | μs | O | |
| WDT pin input pulse width, low | t _{WDL} | V _{DD} = 3.6V, V ₋ = V _{RSENS} = 0 V | 0.5 | | | μs | O | |
| V ₋ pin current for watchdog reset hold time | I _{V-WDR} | V _{DD} = 3.6V, V ₋ = V _{RSENS} = 0 V | - | 0.1 | 0.2 | μA | Q | |

*1 The test circuits for device evaluation. Refer to the section of TEST CIRCUITS for detail information.

*2 Only 2nd step detection type

Ta = 25°C, unless otherwise noted

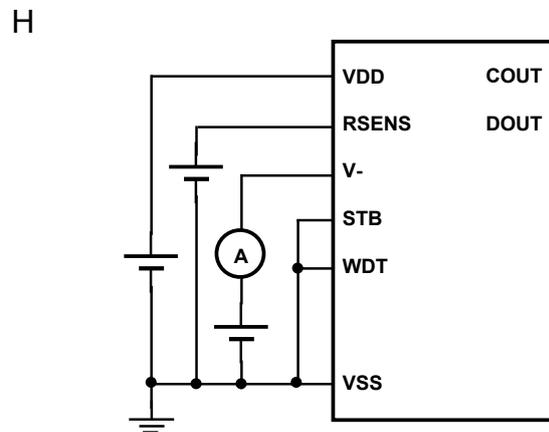
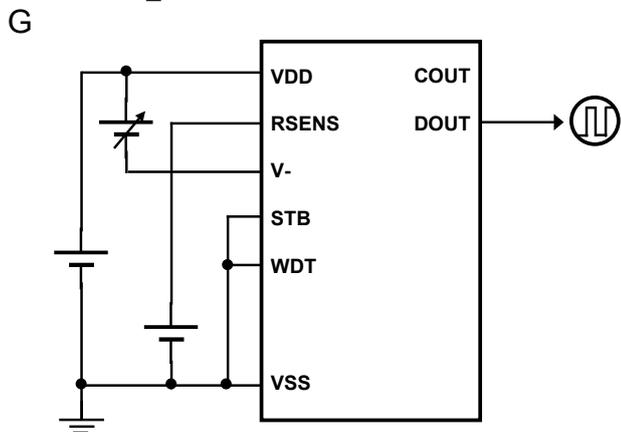
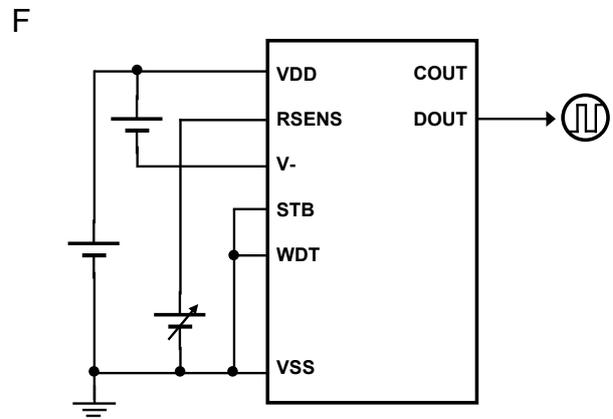
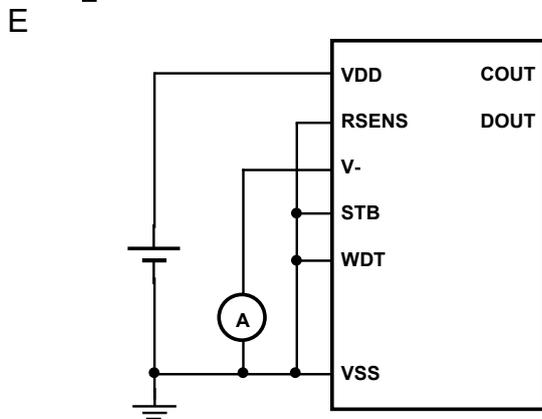
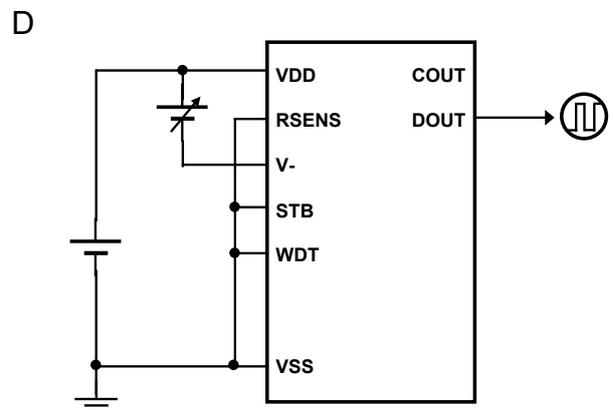
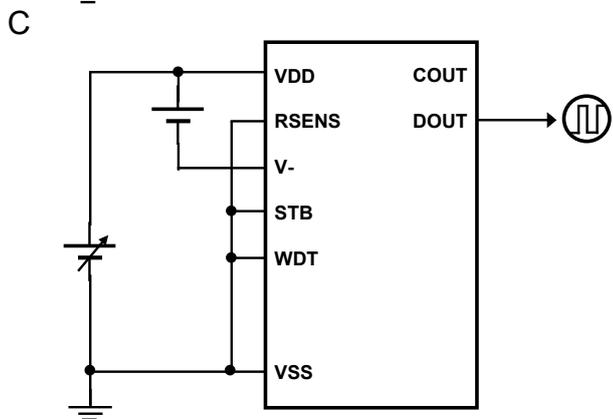
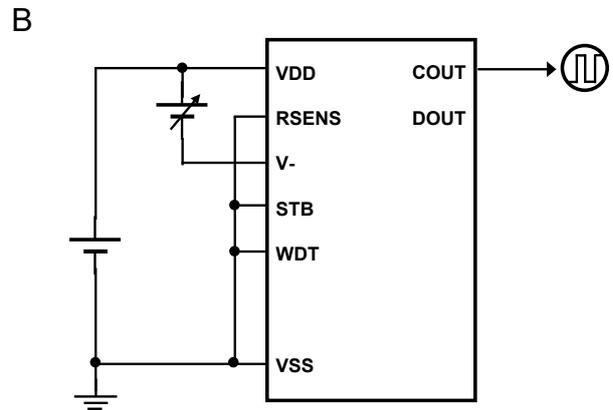
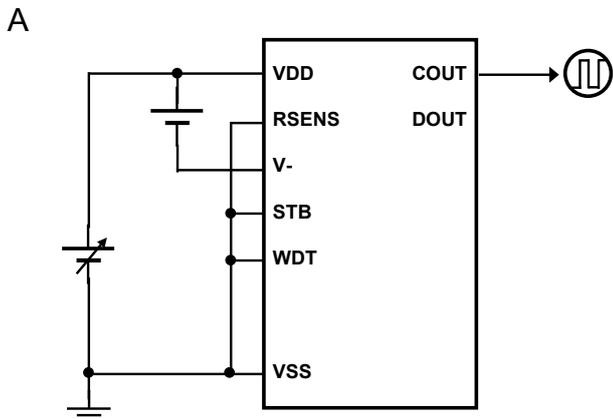
NB7141ZA****E2S Electrical Characteristics (Continued)

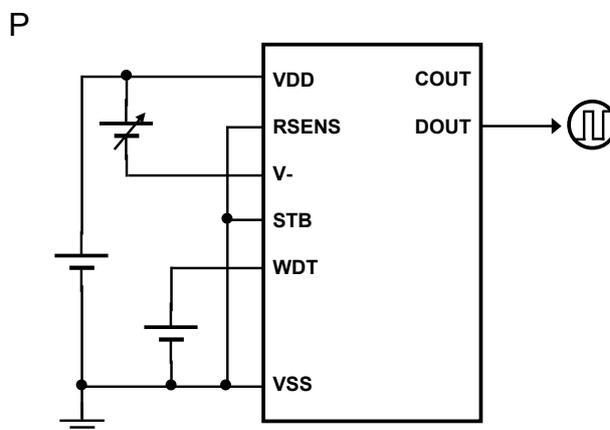
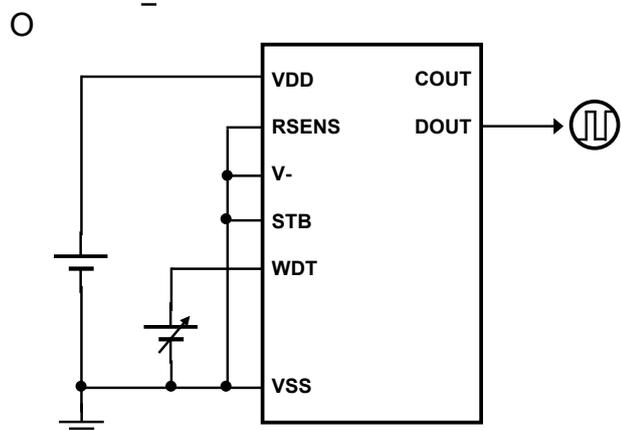
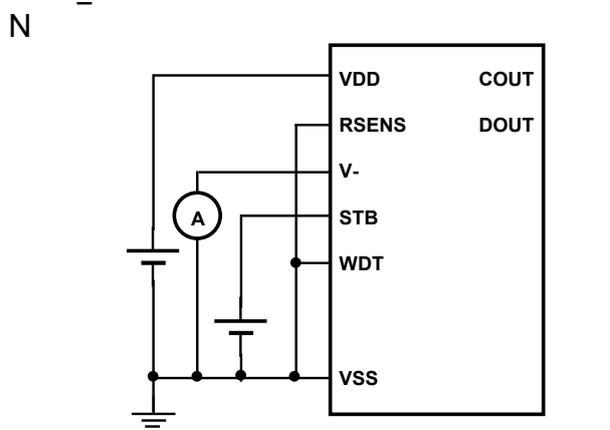
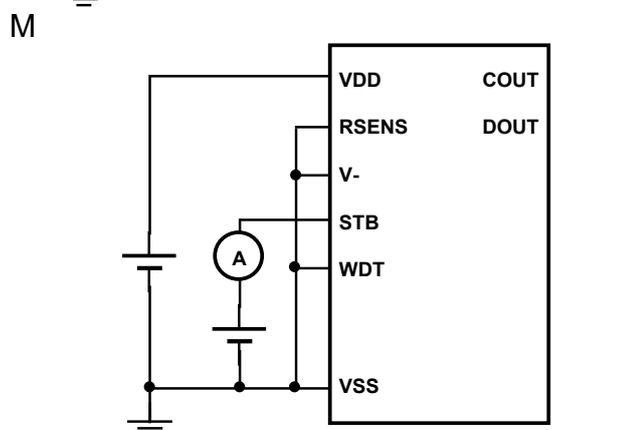
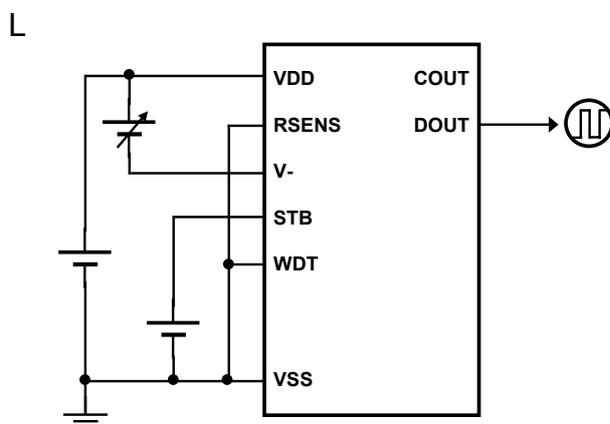
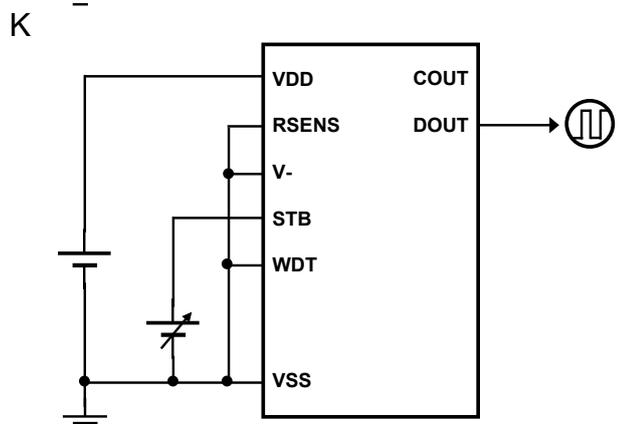
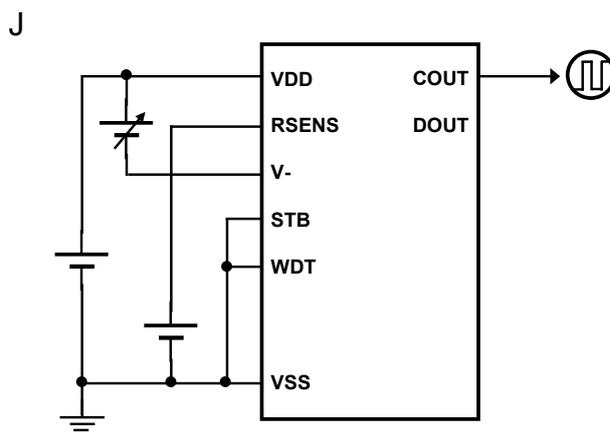
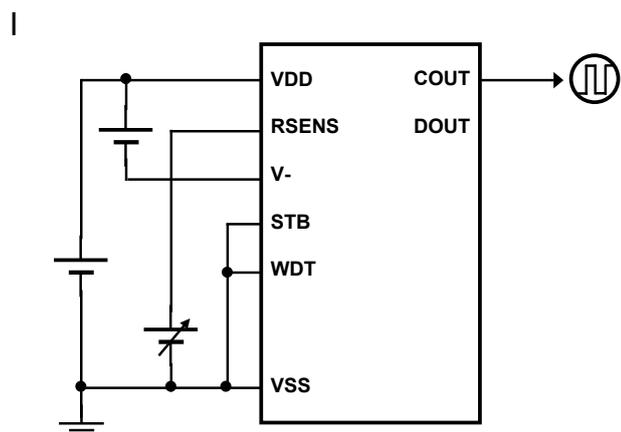
| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit | Remark ^{*1} | |
|---------------------------------------|----------------------|--|-------------------------------------|------|------|------|----------------------|---|
| COUT pin NMOS ON voltage | V _{OL1} | I _{OL} = 50μA, V _{DD} = 4.80 V | | 0.4 | 0.5 | V | R | |
| COUT pin PMOS ON voltage | V _{OH1} | I _{OH} = -50μA, V _{DD} = 3.90 V | 3.4 | 3.7 | | V | S | |
| DOUT pin NMOS ON voltage | V _{OL2} | I _{OL} = 50μA, V _{DD} = 1.90 V | | 0.2 | 0.5 | V | T | |
| DOUT pin PMOS ON voltage | V _{OH2} | I _{OH} = -50μA, V _{DD} = 3.90 V | 3.4 | 3.7 | | V | U | |
| Supply current | I _{DD} | V _{DD} = 3.9 V, V ₋ = 0.0 V | 0 V battery charging: Permission | | 1.9 | 3.8 | μA | V |
| | | | 0 V battery charging: Inhibition | | 2.0 | 4.0 | | |
| Supply current in forced standby mode | I _{FSTB} | V _{DD} = V ₋ = 3.9 V | | | 0.04 | μA | W | |
| Standby current | I _{STANDBY} | V _{DD} = 1.9 V | Overdischarge: Auto release | | | 0.20 | μA | W |
| | | | Overdischarge: Latch | | | 0.04 | | |

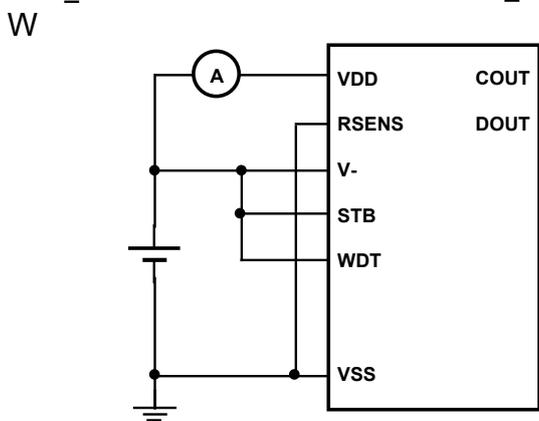
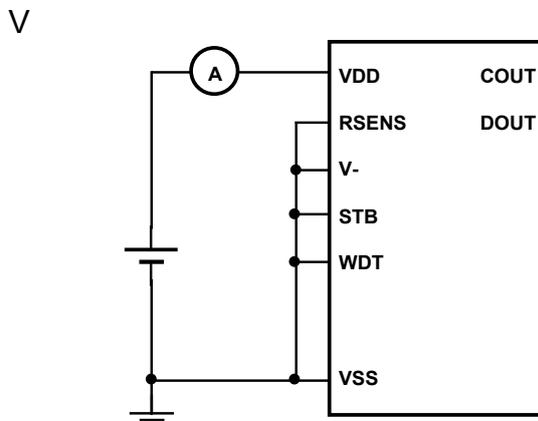
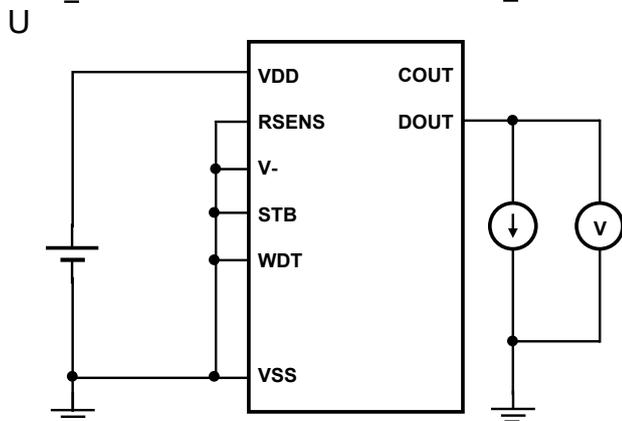
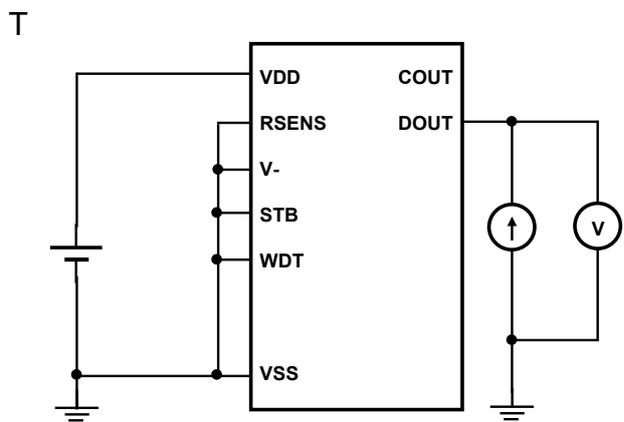
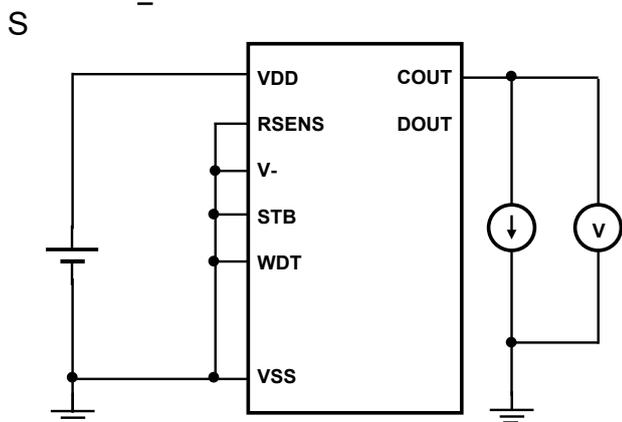
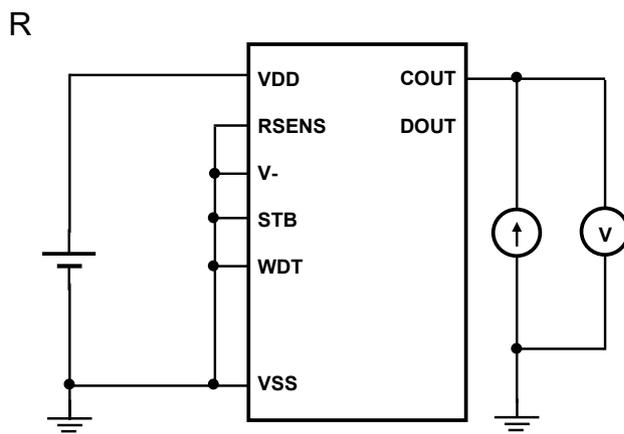
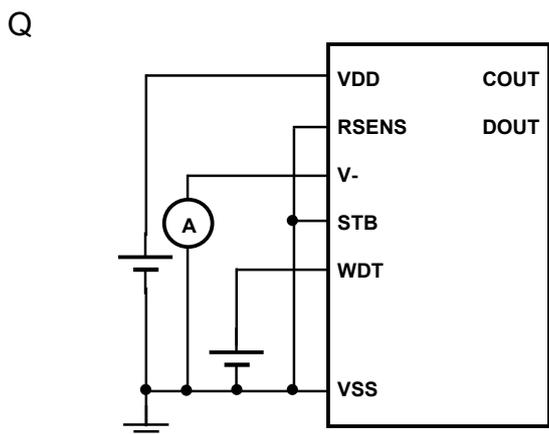
*1 The test circuits for device evaluation. Refer to the section of *TEST CIRCUITS* for detail information.

All test parameters listed in Electrical Characteristics are done under Ta = 25°C only.

Test Circuits







THEORY OF OPERATION

Overcharge Protection

When the overcharge detection delay time (t_{VDET1}) passes under the condition that the VDD pin voltage (V_{DD}) exceeds the overcharge detection voltage (V_{DET1}), this IC enters the over-charge state.

In this state, the COUT pin becomes Low, and the charge control FET is turned off to stop charging. The V- pin voltage (V_-) increases by the Vf voltage (Vf) of the internal parasitic diode than the VSS pin voltage (V_{SS}) because the discharge current flows via the parasitic diode even when the charge control FET is off.

A release from the overcharge state must meet the following pin conditions and delay time according to the selected release type.

| Type | Pin Conditions | Delay Time |
|--------------|--|-------------|
| Auto Release | $V_- < V_{REL4}$ and $V_{DD} < V_{REL1}$ or $V_- > V_{REL4}$ and $V_{DD} < V_{DET1}$ | t_{VREL1} |
| Latch | $V_- > V_{REL4}$ and $V_{DD} < V_{DET1}$ | t_{VREL1} |

Overdischarge Protection

When the overdischarge detection delay time (t_{VDET2}) passes under the condition that the VDD pin voltage (V_{DD}) falls below the over-discharge detection voltage (V_{DET2}), this IC enters the over-discharge state.

In this state, the DOUT pin becomes Low, and the discharge control FET is turned off to stop discharging. The V- pin voltage (V_-) decreases by the Vf voltage (Vf) of the internal parasitic diode than the VSS pin voltage (V_{SS}) because the charge current flows via the parasitic diode even when the discharge control FET is off.

In addition, when V- is pulled up to V_{DD} level and exceeds the charger detection voltage (V_{CHGDET}), the IC enters the standby state. It results in reducing the standby current ($I_{STANDBY}$) to a minimum.

A release from the overdischarge state must meet the following pin conditions and delay time according to the selected release type.

| Type | Pin Conditions | Delay Time |
|--------------|--|-------------|
| Auto Release | $V_- > V_{CHGDET}$ and $V_{DD} > V_{REL2}$ or $V_- < V_{CHGDET}$ and $V_{DD} > V_{DET2}$ | t_{VREL2} |
| Latch | $V_- < V_{CHGDET}$ and $V_{DD} > V_{DET2}$ | t_{VREL2} |

Discharge Overcurrent Protection

To monitor a discharge current, this IC measures a voltage difference of the sense resistor (R_{SENS}) connected between the RSENS and the VSS pins to detect the current value.

This IC has two levels of the discharge overcurrent detection voltage 1/2 ($V_{\text{DET31}} / V_{\text{DET32}}$). When the discharge overcurrent detection delay time (t_{VDET31}) passes under the condition that the discharge current, which is converted through R_{SENS} for current-to-voltage conversion, exceeds V_{DET31} , this IC enters the discharge overcurrent state. In a case where V_{DET32} is enabled, this IC enters the discharge overcurrent state when the discharge overcurrent detection delay time (t_{VDET32}) passes under the condition exceeding V_{DET32} .

In this state, the DOUT pin becomes Low, and the discharge control FET is turned off to shut off the discharge current.

A release from the discharge overcurrent state must meet the following pin condition and delay time according to the selected release type.

| Type | Pin Condition | Delay Time | Remarks |
|----------------|--|--------------------|---|
| Auto Release 1 | $V_- < V_{\text{REL3}}$ (Typ. $V_{\text{DD}} \times 0.800$) | t_{VREL3} | V_- is pulled down to the V_{SS} level inside the IC. ^{Note1} ($R_{\text{SHORT}} = \text{Typ. } 10\text{k}\Omega$) |
| Auto Release 2 | $V_- < V_{\text{REL3}}$ (Typ. 0.700 V) | t_{VREL3} | V_- is pulled down to the V_{SS} level inside the IC. ^{Note1} ($R_{\text{SHORT}} = \text{Typ. } 45\text{k}\Omega$) |
| Latch | $V_- < V_{\text{REL3}}$ (Typ. 0.700 V) | t_{VREL3} | V_- is pulled up to the V_{DD} level inside the IC. ^{Note2} |

Note1: It is possible to release the abnormal condition of the load connected to the battery pack. When the discharge overcurrent release delay time (t_{VREL3}) passes under the condition V_- falls below V_{REL3} , this IC releases from the discharge overcurrent state. V_- can be expressed by the following equation.

$$V_- = V_{\text{CELL}} \times R_{\text{SHORT}} / (R_{\text{SHORT}} + R_{\text{V-}} + R_{\text{LOAD}})$$

V_{CELL} : Battery voltage

R_{SHORT} : Discharge overcurrent release resistance

$R_{\text{V-}}$: External resistor for V_- pin

R_{LOAD} : Load resistance to a battery pack

Note2: When connecting a charger to pull V_- down, this IC releases from the discharge overcurrent state.

Short-circuit Current Protection

To monitor a short-circuit current, this IC measures a voltage difference of the sense resistor (R_{SENS}) connected between the RSENS and the VSS pins to detect the current value. When the short-circuit current, which is converted through R_{SENS} for current-to-voltage conversion, exceeds the short-circuit detection voltage1 (V_{SHORT1}), this IC enters the short-circuit state. But it is possible for this IC to avoid its state when the short-circuit current falls below V_{SHORT1} within the short-circuit detection delay time (t_{SHORT}).

In this state, the DOUT pin becomes Low, and the discharge control FET is turned off to shut off the short-circuit current.

A release from the short-circuit state must meet the same condition and delay time as the discharge overcurrent protection.

Charge Overcurrent Protection

To monitor a charge current, this IC measures a voltage difference of the sense resistor (R_{SENS}) connected between the RSENS and the VSS pins to detect the current value. When the charge overcurrent detection delay time (t_{VDET4}) passes under the condition that the charge current, which is converted through RSENS for current-to-voltage conversion, falls below the charge overcurrent detection voltage (V_{DET4}), this IC enters the charge overcurrent state.

In this state, the COUT pin becomes Low, and the charge control FET is turned off to shut off the charge current.

A release from the charge overcurrent state must meet the following pin condition and delay time according to the selected release type.

| Type | Pin Condition | Delay Time | Remarks |
|--------------|-------------------------|--------------------|---|
| Auto Release | $V_- > V_{\text{REL4}}$ | t_{VREL4} | V- is pulled up to the V_{DD} level inside the IC. ^{Note} |

Note: By disconnecting the charger, this IC releases from the charge overcurrent state.

0 V Battery Charging

This IC has the selectable charging function for the battery discharged to 0 V.

0 V Battery Charge Function “Permission”

This function allows to charge to the 0 V battery by connecting the charger with the minimum charging voltage (V_{STCHG}) and more.

0 V Battery Charge Function “Inhibition”

This function inhibits to charge to the battery with the 0 V-battery charging inhibition voltage (V_{NOCHG}) or less even when connecting the charger.

Forced-standby Function

When the forced standby detection delay time (t_{STBD}) passes under the condition that the STB pin voltage (V_{STB}) exceeds the forced standby detection voltage (V_{STBD}), this IC enters the forced standby detected state.

In this state, the IC turns off the charge and the discharge control FETs and the V- pin is pulled up to the V_{DD} level by an internal resistor, here the V- pin pullup resistance is $R_{\text{V-2}}$.

After that, the IC enters the forced standby state when V- exceeds the charger detection voltage (V_{CHGDET}). It results in reducing the supply current (I_{FSTB}) to a minimum.

A release from the forced standby state must meet the following pin condition and delay time according to the selected release type.

| Type | Pin Condition | Delay Time | Remarks |
|-------|---------------------------|-------------------|---|
| Latch | $V_- < V_{\text{CHGDET}}$ | t_{STBR} | V- is required to exceed V_{SHORT2} once for the release. The IC should not enter the forced standby state under connecting the charger. |

Watchdog Timer Function

During the watchdog timeout period (t_{WD}), the timeout counter is reset when the WDT pin receives an edge signal. Conversely, the watchdog timer is reset state when not receive it.

In this state, the IC turns off the charge and the discharge control FETs.

A release from the forced reset state must meet the following pin condition and delay time according to the selected release type.

| Type | Pin Condition | Delay Time | Remarks |
|--------------|------------------|------------|---|
| Auto release | - | t_{WR} | Immediately after forced reset detected, watchdog timer reset release delay time (t_{WR}) counting start. |
| V- rising | $V > V_{SHORT2}$ | t_{WR} | After the system power is depleted, watchdog timer reset release delay time (t_{WR}) counting start. |

Timing Chart

Overcharge

Charger

Load

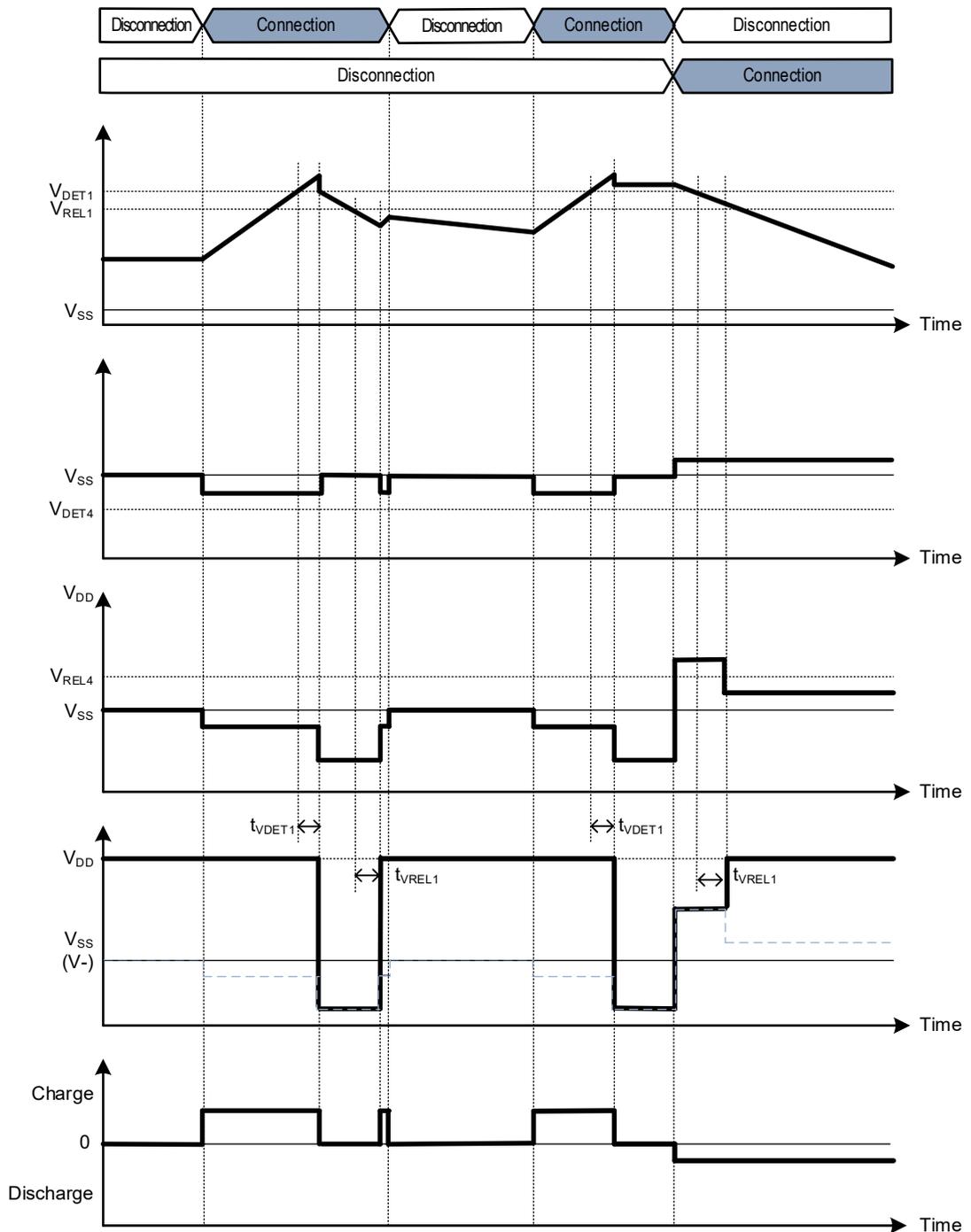
VDD

RSENS

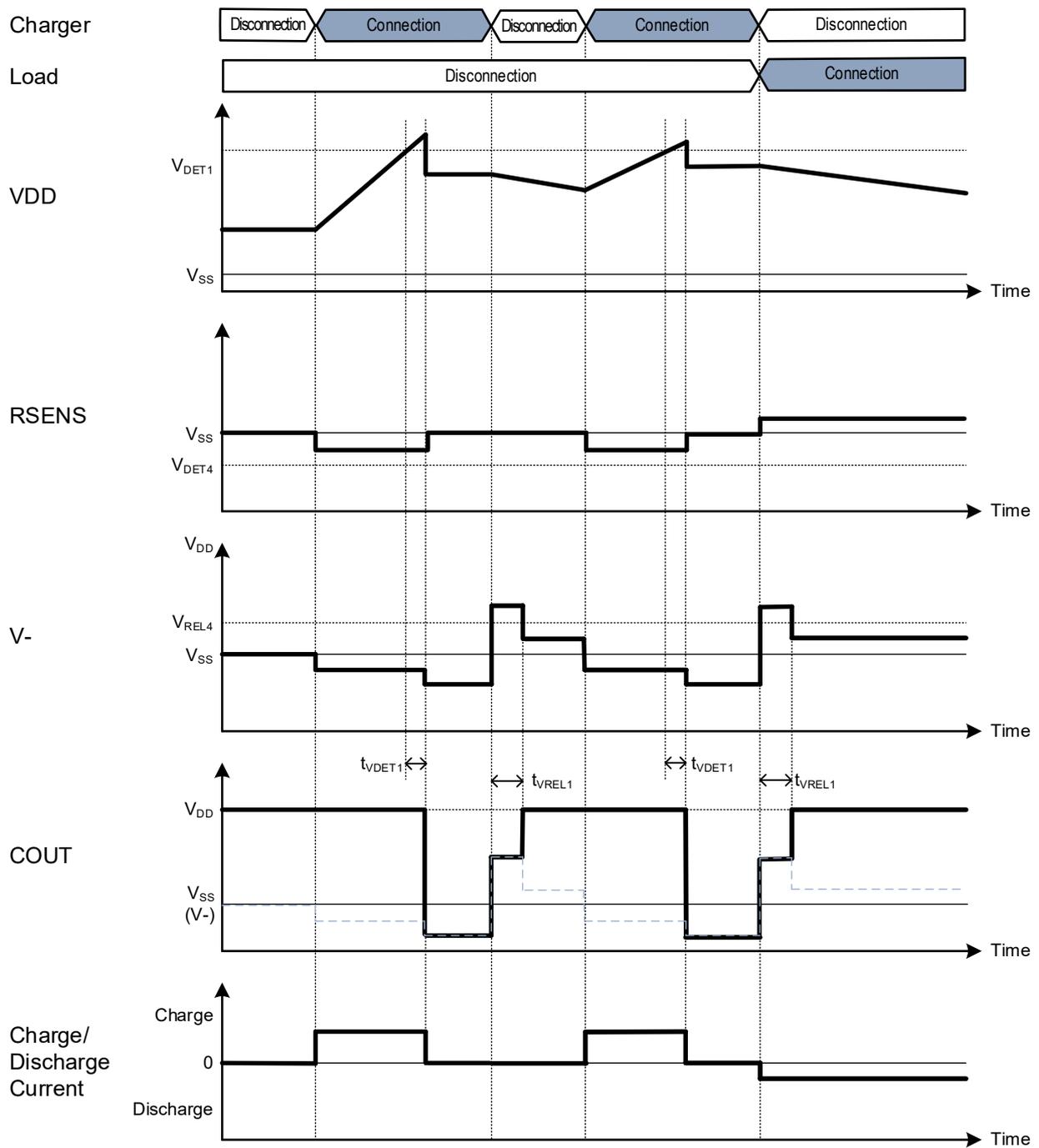
V-

COUT

Charge/
Discharge
Current



Overcharge (Auto Release) Timing Chart



Overcharge (Latch) Timing Chart

Overdischarge

Charger

Load

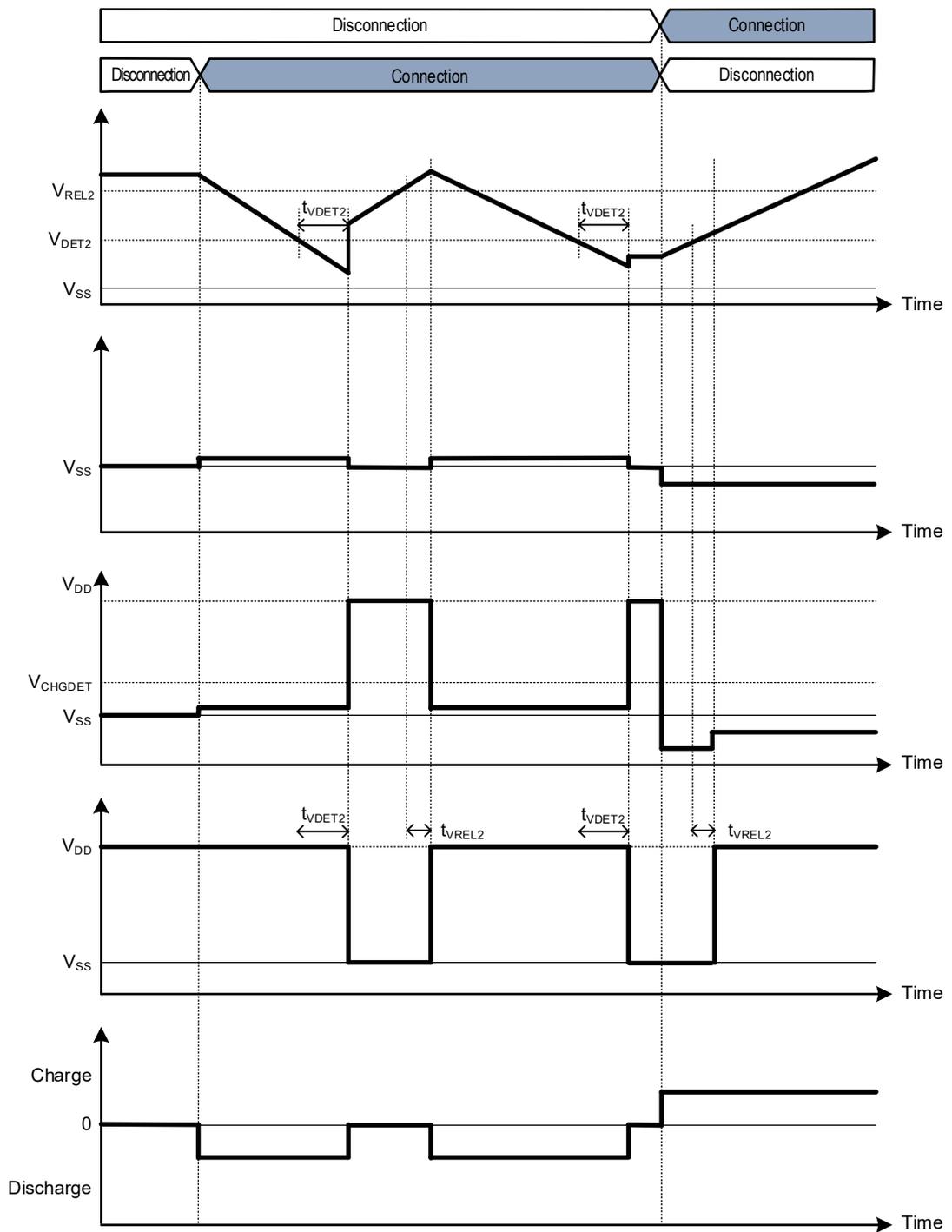
VDD

RSENS

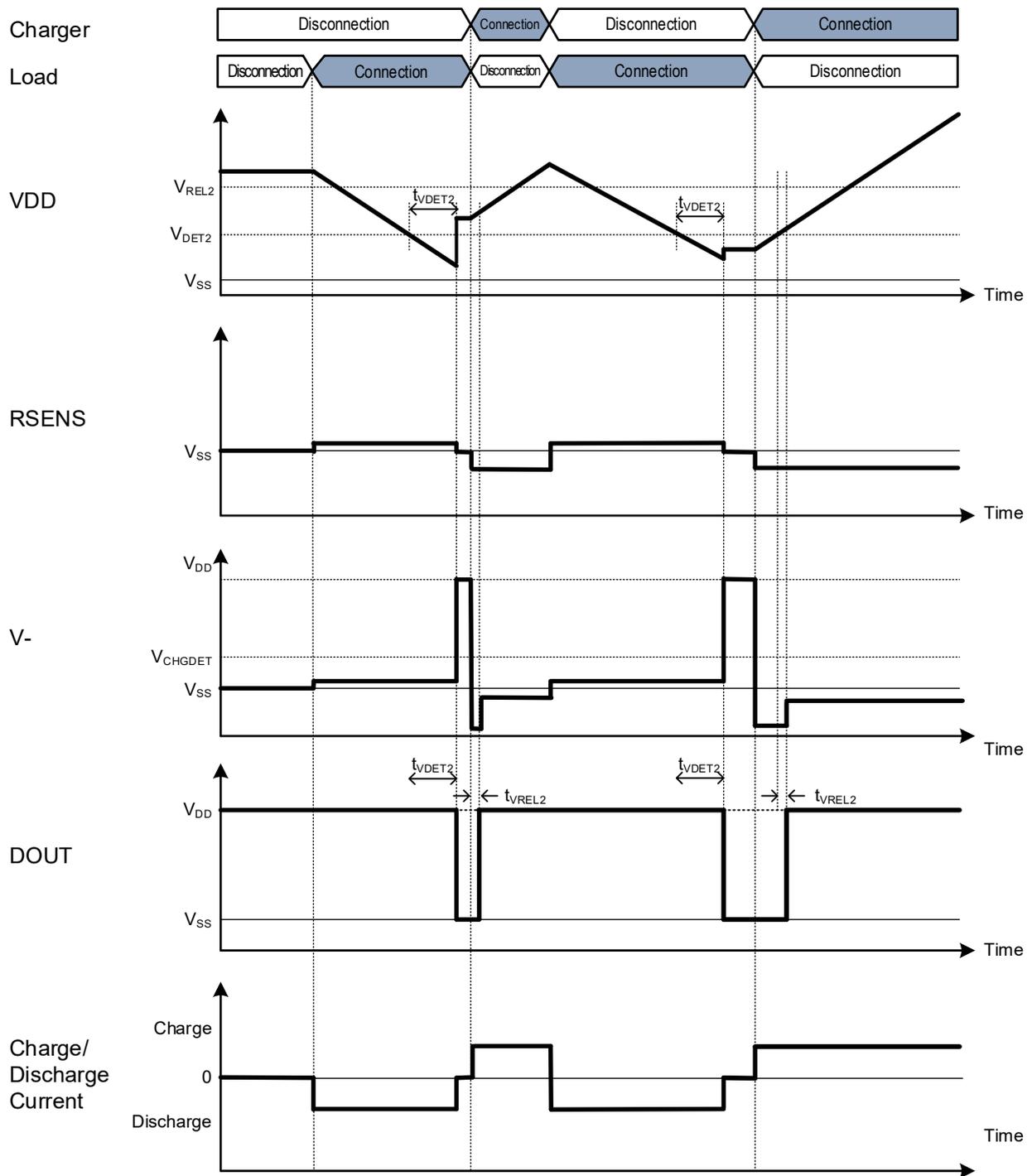
V-

DOUT

Charge/
Discharge
Current

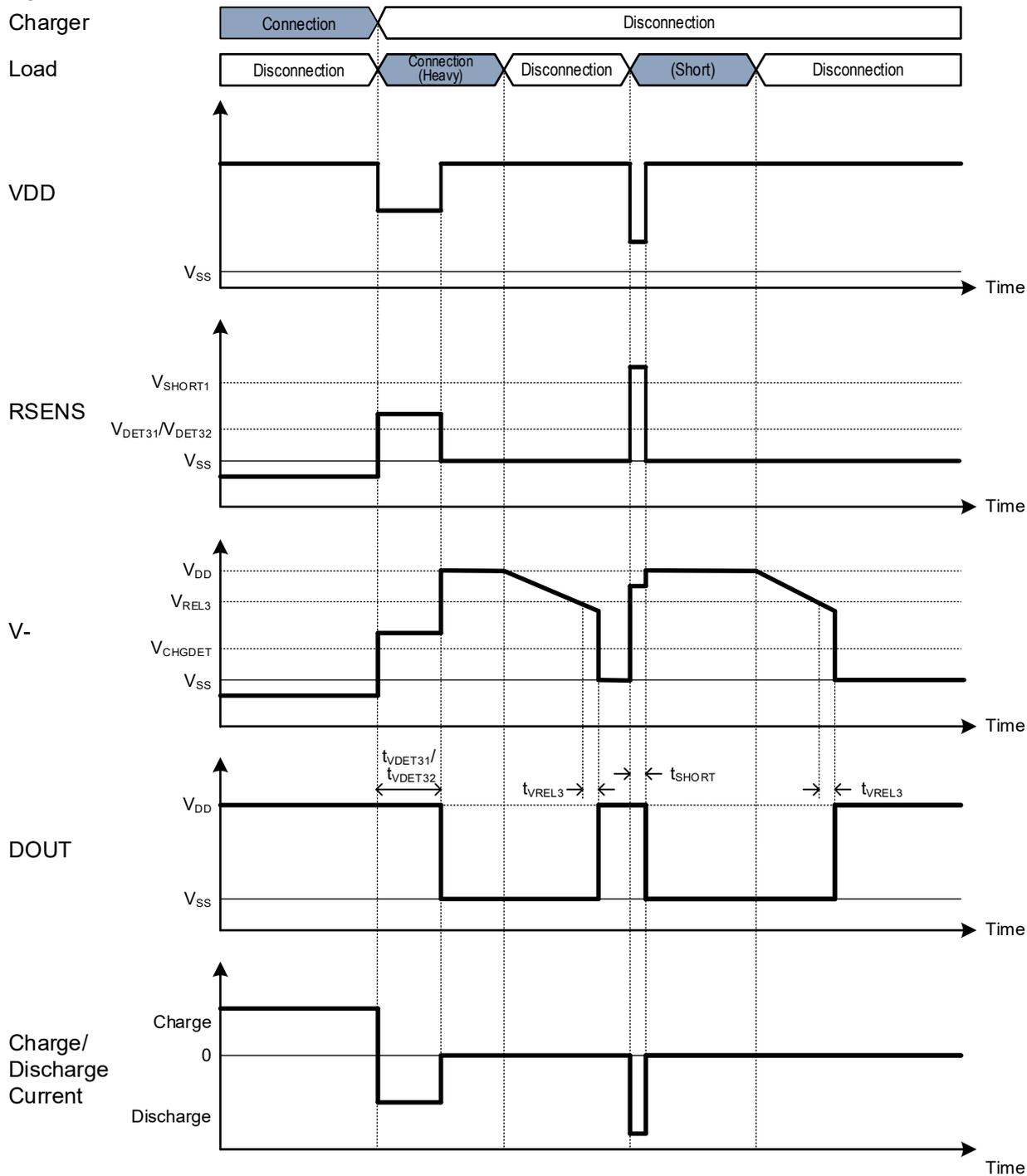


Overdischarge (Auto Release) Timing Chart

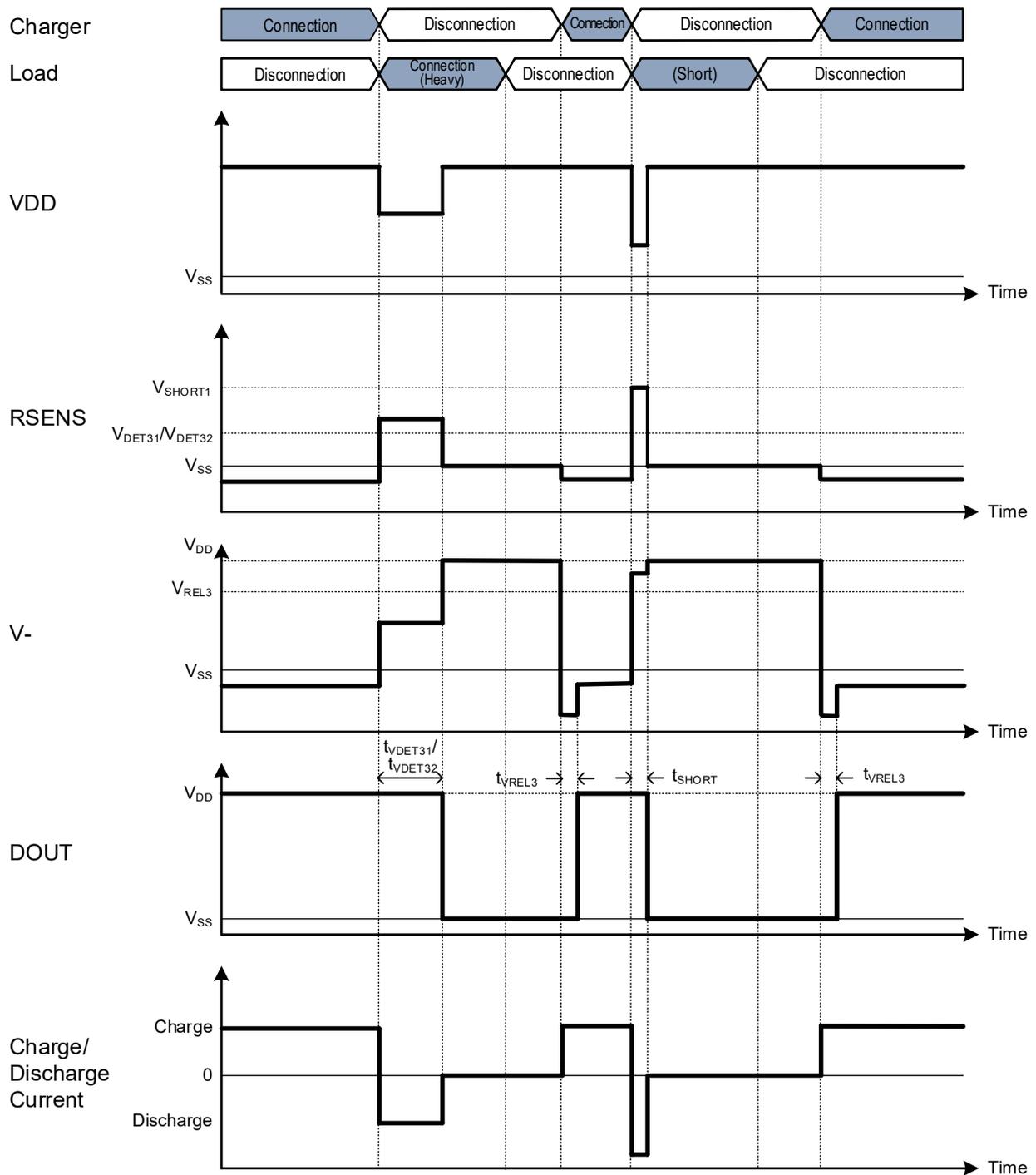


Overdischarge (Latch) Timing Chart

Discharge Overcurrent

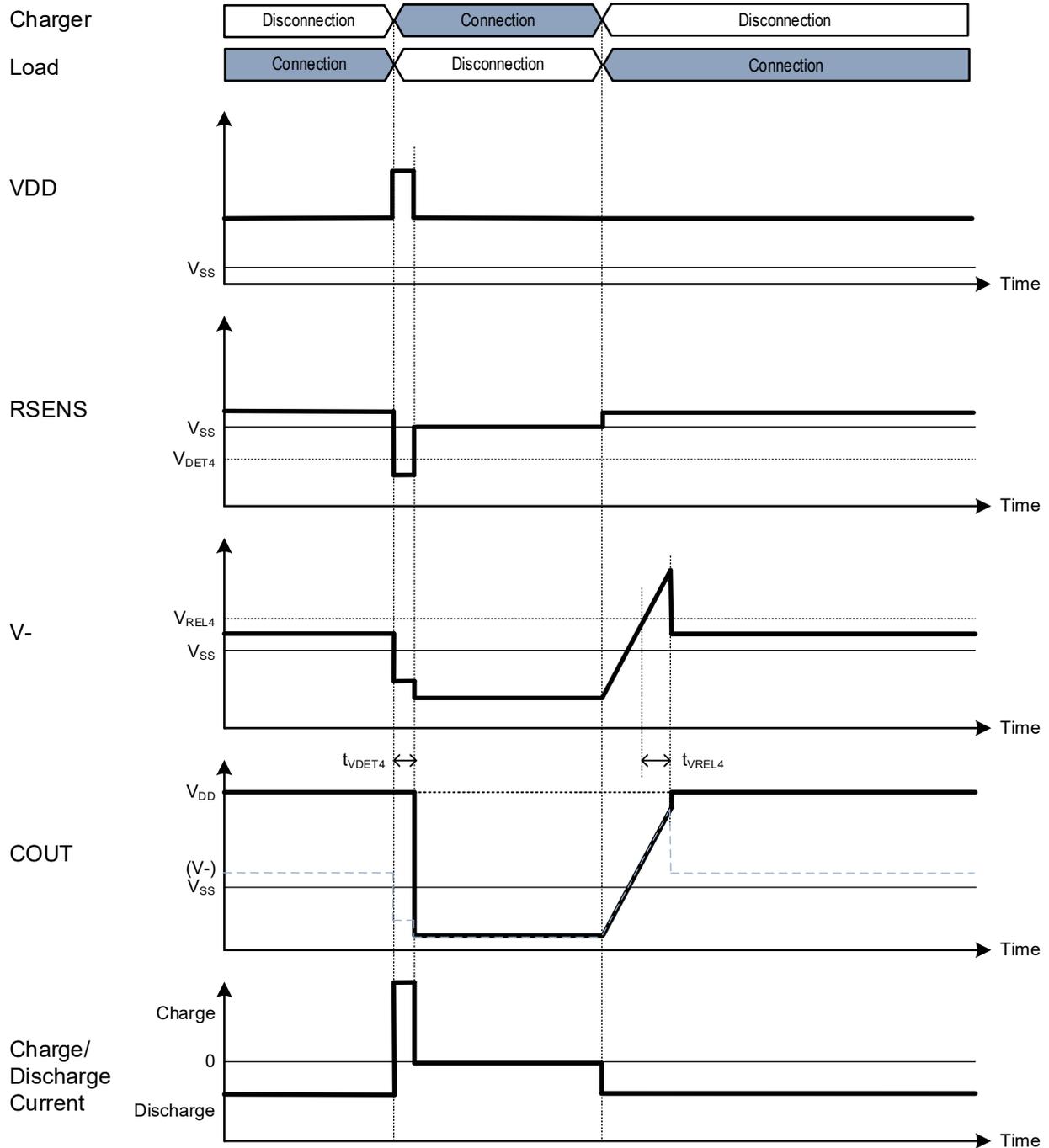


Discharge Overcurrent (Auto Release) Timing Chart



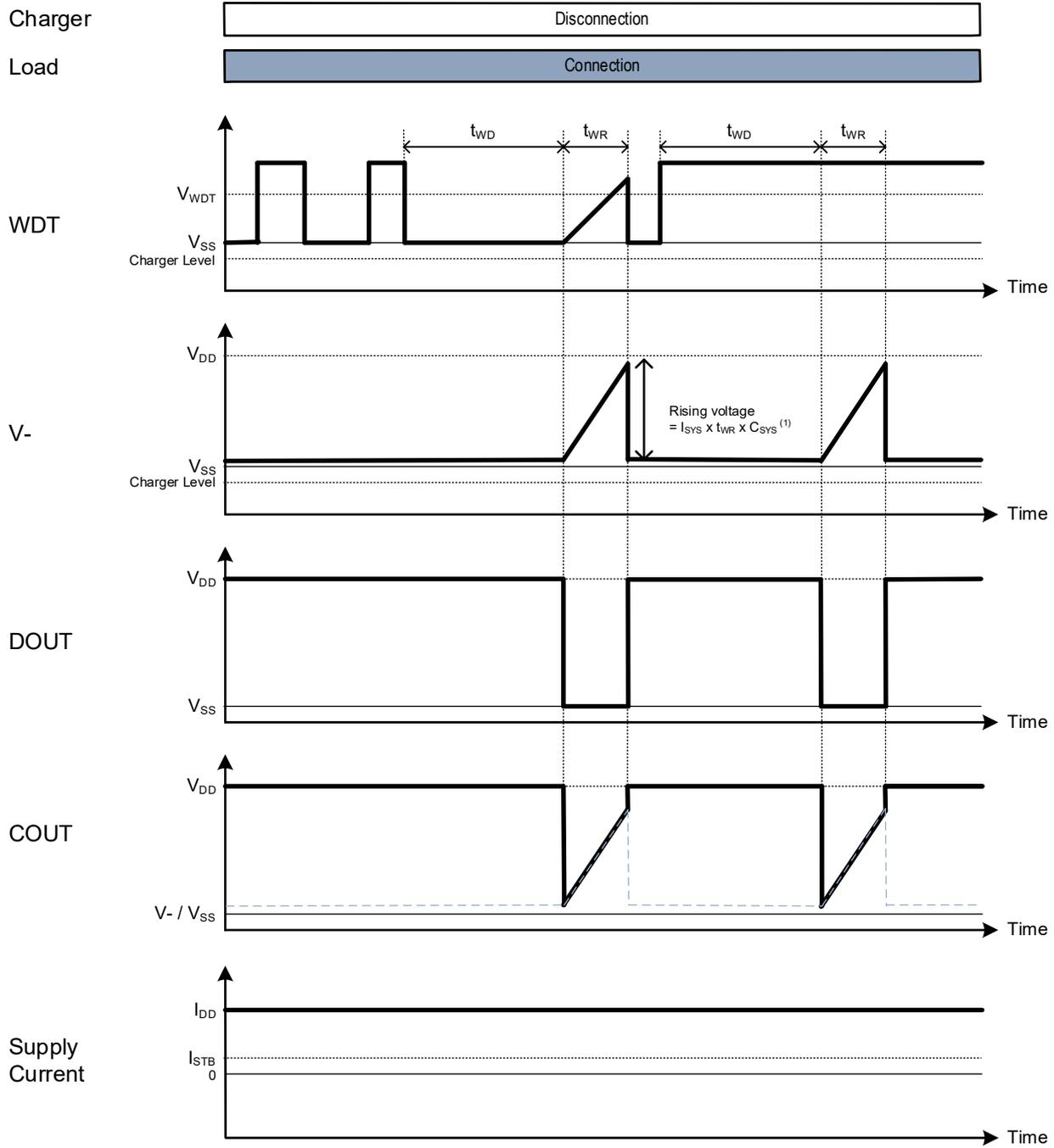
Discharge Overcurrent (Latch) Timing Chart

Charge Overcurrent



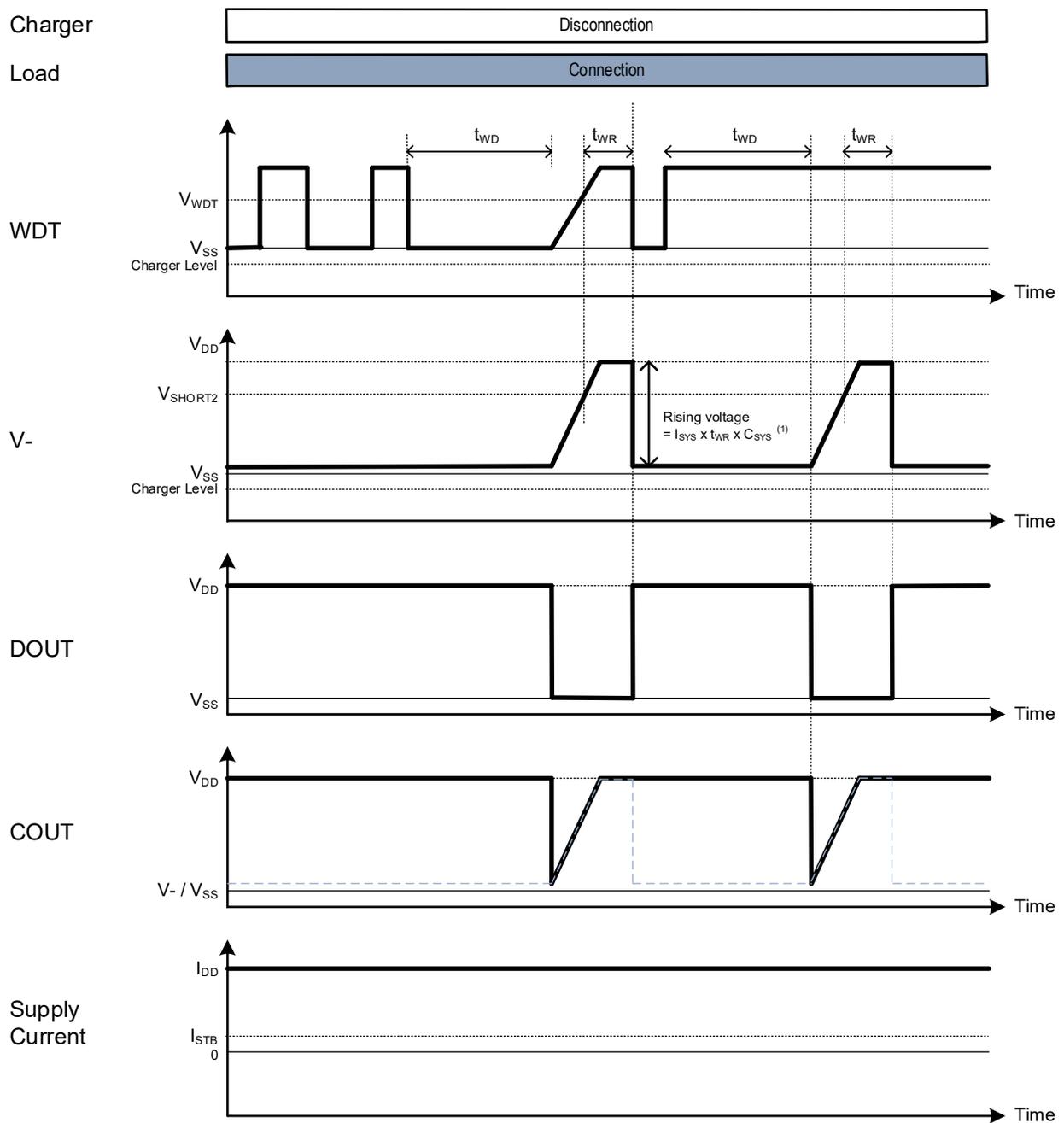
Charge Overcurrent Timing Chart

Watchdog Timer



Watch Dog Timer (Auto release) Timing Chart

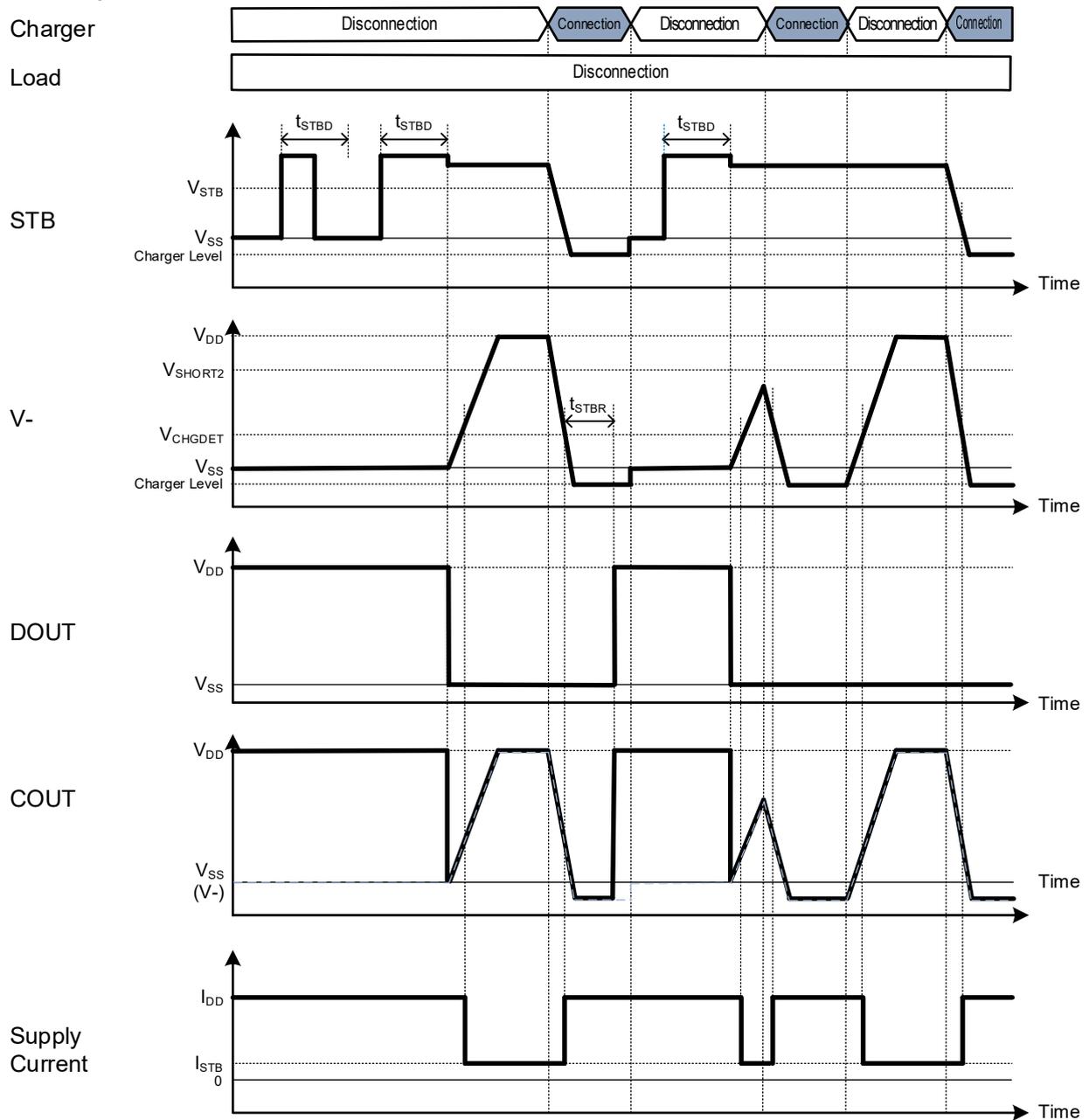
⁽¹⁾ I_{SYS}, C_{SYS} : Refer to the chapter "TYPICAL APPLICATION CIRCUIT".



Watch Dog Timer (V- rising) Timing Chart

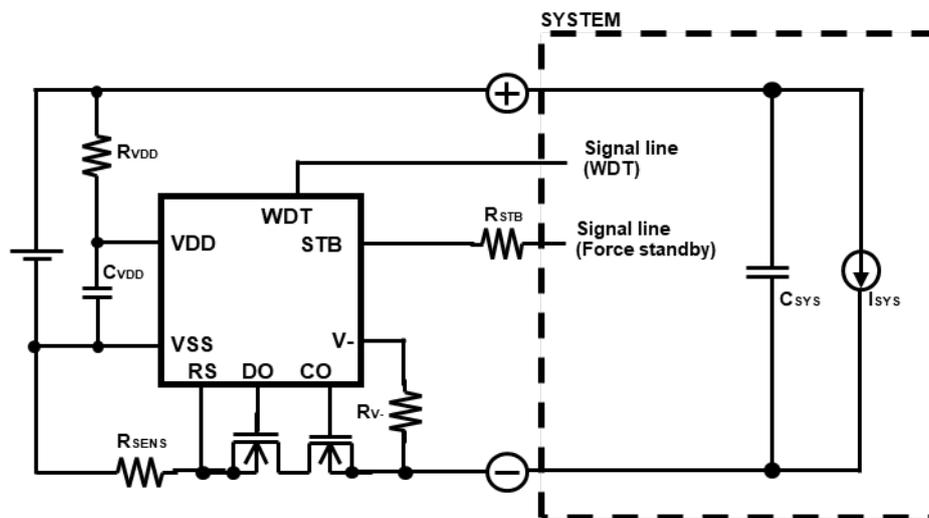
⁽¹⁾ I_{SYS}, C_{SYS} : Refer to the chapter "TYPICAL APPLICATION CIRCUIT".

Forced Standby Mode



Forced Standby Mode Timing Chart

TYPICAL APPLICATION CIRCUIT



NB7141ZA Typical Application Circuit

External Components

| Symbol | Min. | Typ. | Max. |
|----------------|--------|--------|--------|
| Resistor | | | |
| R_{VDD}^{*1} | - | 330Ω | 1kΩ |
| R_{V-}^{*1} | - | 1.0kΩ | 1.3kΩ |
| R_{SENS} | - | 1.5mΩ | - |
| R_{STB} | - | 1.0kΩ | 10kΩ |
| Capacitor | | | |
| C_{VDD} | 0.01μF | 0.10μF | 1.00μF |

*1 The total resistance of R_{VDD} and R_{V-} must be 1kΩ or more.

Technical Notes Related to External Components

- The voltage fluctuation is stabilized with R_{VDD} and C_{VDD} . If a R_{VDD} is too large, the detection voltage rises by the conduction current at detection. To stabilize the operation, it is recommended to use a resistor of 1k Ω or less for R_{VDD} and a capacitor of 0.01 μ F to 1.00 μ F for C_{VDD} .
- R_{VDD} and R_{V-} serve as a current limit resistor when the battery pack is charged with reversed polarity, or a voltage of the connected charger is more than the absolute maximum rating. When using a small resistor for R_{VDD} and R_{V-} , the device's power dissipation might be exceeded. Therefore, a total of R_{VDD} and R_{V-} must be 1k Ω or more. When using a large resistor for R_{V-} , the charger might not be released by re-connecting to the battery pack after the over-discharge detection. Therefore, R_{V-} must be 1.3k Ω or less. Production variation and temperature properties are included in the value. R_{SENS} is a resistor for sensing an overcurrent. If the resistance value is too large, power loss becomes also large. By the overcurrent, if the R_{SENS} is not appropriate, the power loss may be beyond the power dissipation of R_{SENS} . Choose an appropriate R_{SENS} according to the cell specification.
- The typical application circuit diagrams are just examples. This circuit performance largely depends on the PCB layout and external components. In the actual application, fully evaluation is necessary.
- If the positive terminal and the negative terminal of the battery pack are short even though the device has the short protection circuit, a large current may flow through the FET during the delay time until detecting the short circuit. Therefore, select an appropriate FET with large enough current capacitance to endure the large current during the delay time.

Selection of External Sense Resistor and MOSFET

Short mode is detected by the current base or the relation between V_{DD} at short and total on resistance of external MOSFETs for C_{OUT} and D_{OUT} . When a short circuit detection is required with the current determined by V_{SHORT1} , V_{SHORT2} , and R_{SENS} , the next formula must be true, otherwise, the short current limit becomes $(V_{SHORT2}) / (R_{SENS} + R_{SS} (on))$.

$$\frac{V_{SHORT2}}{R_{SENS} + R_{SS}(on)} \geq \frac{V_{SHORT1}}{R_{SENS}}$$

V_{SHORT1} = Threshold value of detecting short circuit using R_{SENS} terminal [V]

V_{SHORT2} = Threshold value of detecting short circuit using $V-$ terminal [V]

R_{SENS} = External current sense resistance [Ω]

$R_{SS} (on)$ = external MOSFETs' total ON resistance [Ω]

In the short mode, a short current is determined by the relation between R_{SENS} and V_{SHORT1} value.

TECHNICAL NOTES

A peripheral component or the device mounted on PCB should not exceed a rated voltage, a rated current or a rated power. When designing a peripheral circuit, please be fully aware of the following points.

- Please evaluate the product at the PCB level before use, as some symptoms may remain that cannot be confirmed by the evaluation at the IC level.
- When using any coating or underfill to improve moisture resistance or joining strength, evaluate them adequately before using. In certain materials or coating conditions, corrosion by contained constituents, current leakage by moisture absorption, crack and delamination by physical stress can happen. If the curing temperature of the coating material or underfill material exceeds the absolute maximum rating, the electrical characteristics of this product may change.
- When performing X-ray inspection in mass production process and evaluation build stage such as the product functions and characteristics confirmation, please confirm X-ray irradiation does not exceed 1.5Gy (absorbed dose for air).

REVISION HISTORY

| Date | Version | Changes |
|--------------------|---------|---|
| June 20, 2022 | 1.00 | First public release |
| July 22, 2022 | 1.10 | Corrected mistakes in writing. |
| August 1, 2022 | 1.20 | Added Electrostatic Discharge (ESD) ratings. |
| September 22, 2022 | 1.30 | Updated timing diagrams in the timing chart section. |
| November 7, 2022 | 1.40 | Deleted marking information from ORDER INFORMATION and PKG INFORMATION and merge them as Marking Specification. Updated timing diagrams in the timing chart section. |

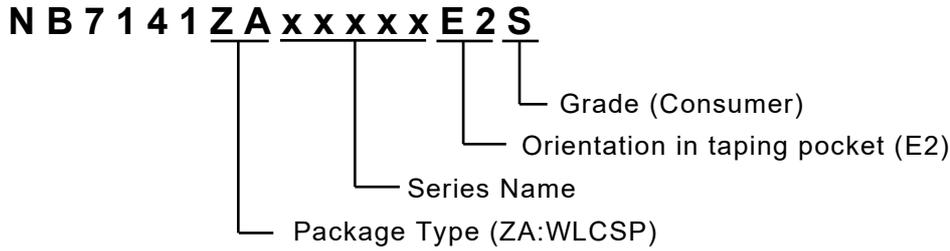


| | Set Voltage [V] / Delay Time [ms] | | | | | | | | | | | | | | | | Optional Function | | | | |
|------------------|-----------------------------------|-----------------------|-----------------------|-----------------------|-------------------------|-------------------------|------------|-----------------------|------------|-------------------------|-------------|-----------------------|-----------|--------------------|----------|------|--------------------|------------------------------------|-------------------------------|--|----------------------------------|
| | V_{DET1} / t_{DET1} | V_{REL1} / t_{RET1} | V_{DET2} / t_{DET2} | V_{REL2} / t_{REL2} | V_{DET31} / t_{DET31} | V_{DET32} / t_{DET32} | t_{REL3} | V_{DET4} / t_{DET4} | t_{REL4} | V_{SHORT} / t_{SHORT} | V_{NOCHG} | V_{STBD} / t_{STBD} | t_{TBR} | V_{WDT} / t_{WD} | t_{WR} | Unit | Overcharge Release | Overdischarge Release ¹ | Discharge Overcurrent Release | Discharge Overcurrent Detection ² | OV Battery Charging ³ |
| NB7141ZA205EHE2S | 4.525 | - | 3.100 | - | 0.0110 | - | - | -0.0400 | - | 0.046 | 2.00 | 1.20 | - | 0.65 | - | V | Latch | Latch | Auto2 | No | No |
| | 1024 | 16.0 | 128 | 1.05 | 4096 | - | 9.0 | 10.0 | 4.0 | 0.28 | - | 300 | 33 | 25.6[s] | 1024 | ms | | | | | |
| NB7141ZA205EME2S | 4.525 | - | 3.100 | - | 0.0110 | - | - | -0.0400 | - | 0.046 | 2.00 | 0.65 | - | 0.65 | - | V | Latch | Latch | Auto1 | No | No |
| | 1024 | 16.0 | 128 | 1.05 | 4096 | - | 9.0 | 10.0 | 4.0 | 0.28 | - | 300 | 33 | 25.6[s] | 1024 | ms | | | | | |
| NB7141ZA205ENE2S | 4.525 | - | 3.100 | - | 0.0110 | - | - | -0.0400 | - | 0.046 | 2.00 | 0.65 | - | 0.65 | - | V | Latch | Latch | Auto1 | No | Yes |
| | 1024 | 16.0 | 128 | 1.05 | 4096 | - | 9.0 | 10.0 | 4.0 | 0.28 | - | 300 | 33 | 25.6[s] | 1024 | ms | | | | | |
| NB7141ZA206HRE2S | 4.200 | 4.000 | 2.100 | 2.300 | 0.0500 | - | - | -0.0170 | - | 0.060 | 1.00 | 0.65 | - | 0.65 | - | V | Auto | Auto | Auto1 | No | No |
| | 1024 | 16.0 | 32 | 1.05 | 1024 | - | 9.0 | 17.0 | 4.0 | 0.28 | - | 50 | 4.5 | 1.024[s] | 200 | ms | | | | | |

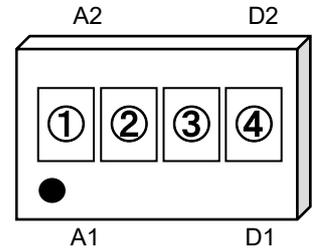
¹ Overdischarge Release Conditions,
 Auto (Auto Release): Cell voltage > V_{REL2}
 Latch : Cell voltage > V_{DET2} under charger connection

² Yes : Available No: Unavailable

³ Yes : Permission No: Inhibition



PKG: WLCSP-8-P10



①② : Product code (See below)

③④ : Lot No.(series)

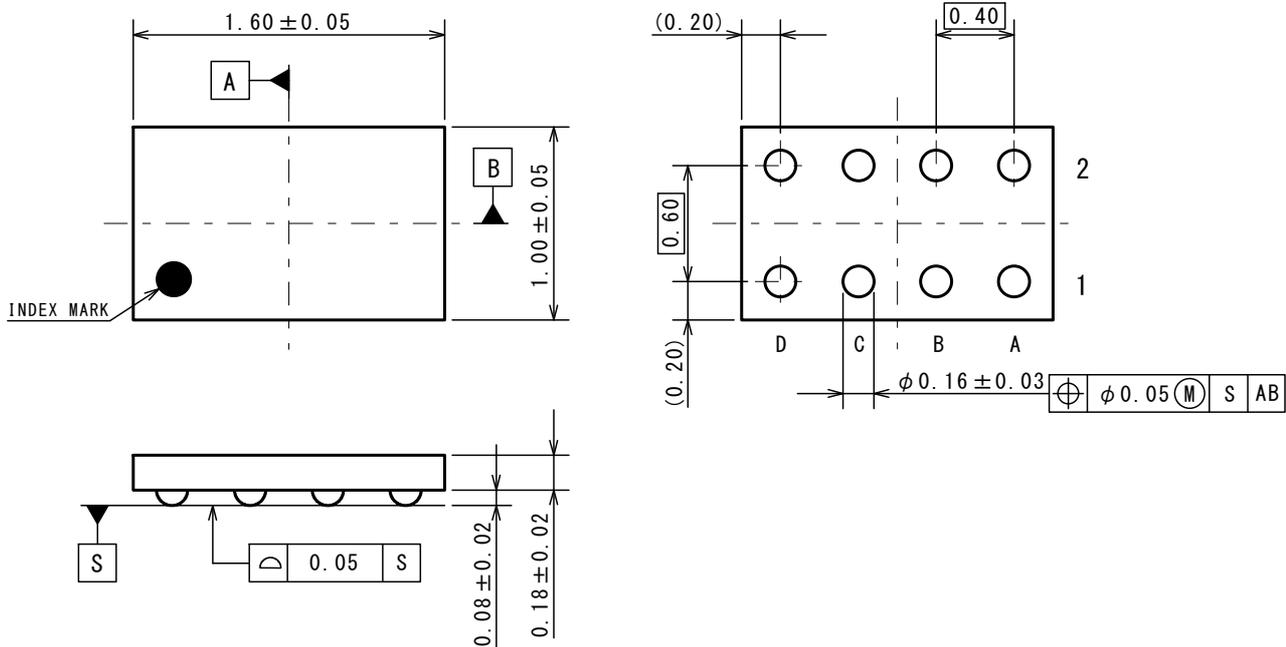
NOTICE

There can be variation in the marking when different AOI (Automated Optical Inspection) equipment is used. In the case of recognizing the marking characteristic with AOI, please contact our sales or distributor before attempting to use AOI.

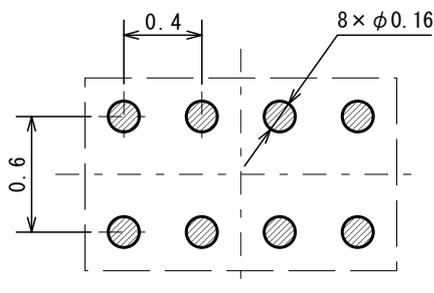
| Product Name | ① ② |
|---------------|------------|
| NB7141ZA205EH | U 0 |
| NB7141ZA205EM | U 1 |
| NB7141ZA205EN | U 2 |
| NB7141ZA206HR | U 3 |

■ PACKAGE DIMENSIONS

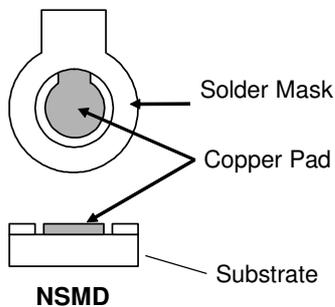
UNIT: mm



■ EXAMPLE OF SOLDER PADS DIMENSIONS



Recommended Land Pattern



| NSMD Pad Definition | | |
|-----------------------------------|------------|---------------------|
| Pad definition | Copper Pad | Solder Mask Opening |
| NSMD (Non-Solder Mask defined) | 0.16mm | MIN. 0.26mm |

*) Pad Layout and size can modify by customers material, equipment and method.

*) Please adjust pad layout according to your conditions.

*) Recommended Stencil Aperture Size: $\phi 0.26$ mm

Nisshinbo Micro Devices Inc.

WLCSP-8-P10

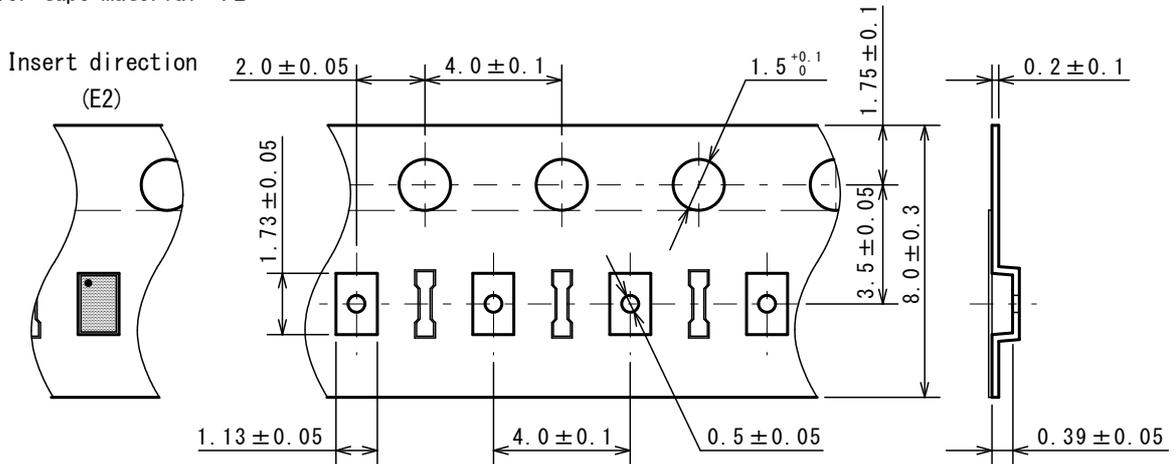
PI-WLCSP-8-P10-E-B

■ PACKING SPEC

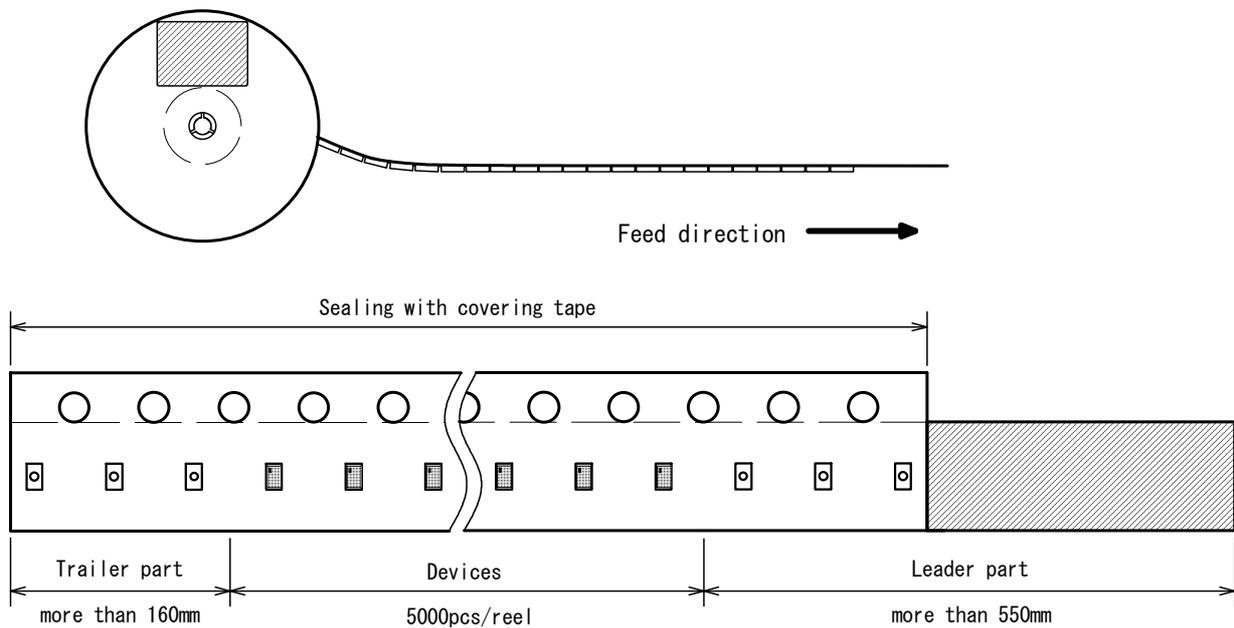
UNIT: mm

(1) Taping dimensions / Insert direction

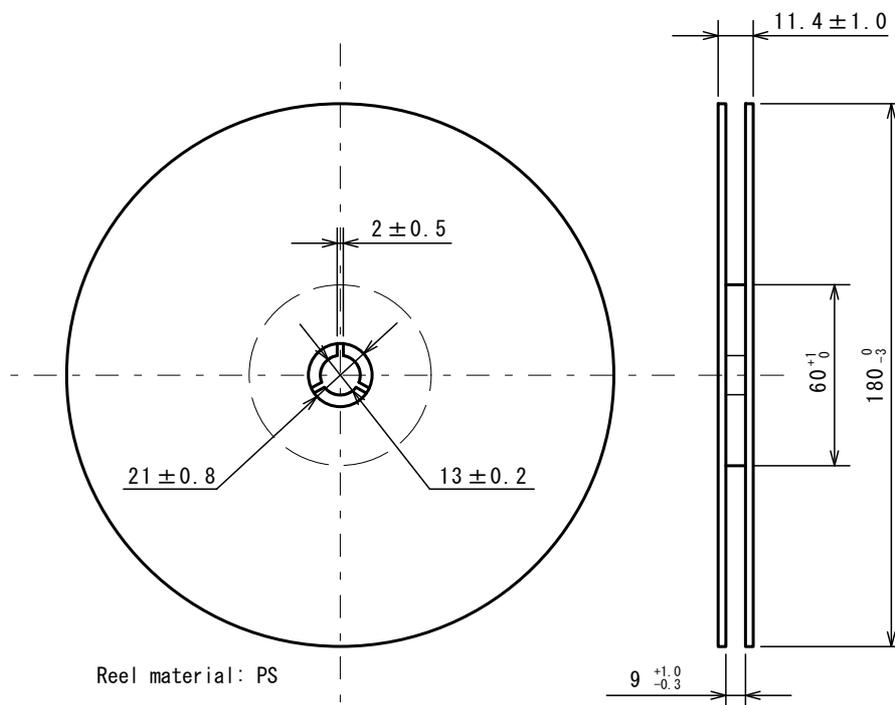
Carrier tape material: PC
Cover tape material: PE



(2) Taping state



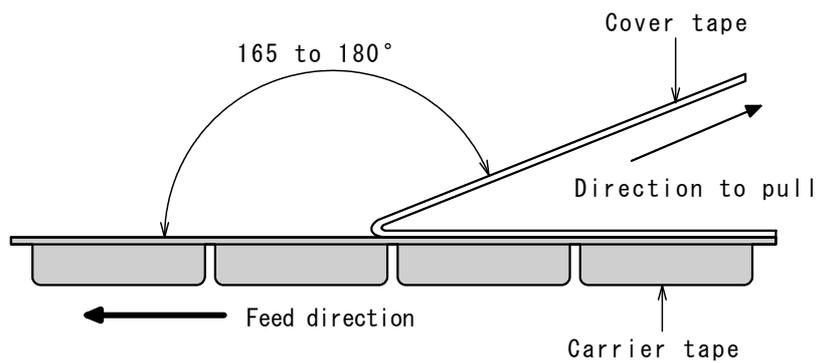
(3) Reel dimensions



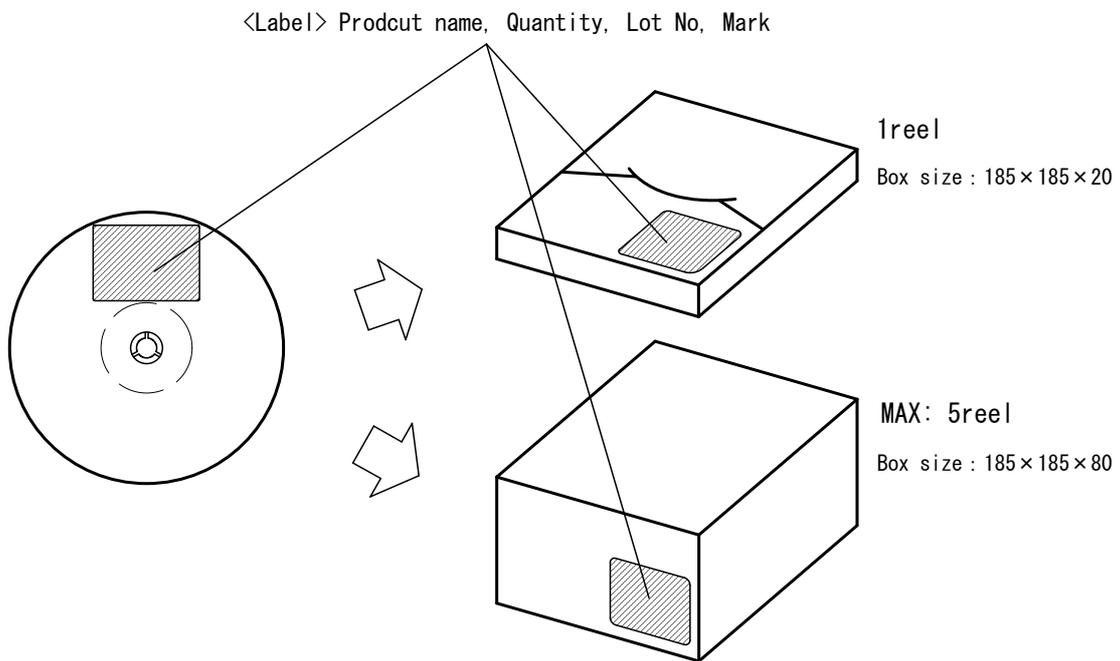
(4) Peeling strength

Peeling strength of cover tape

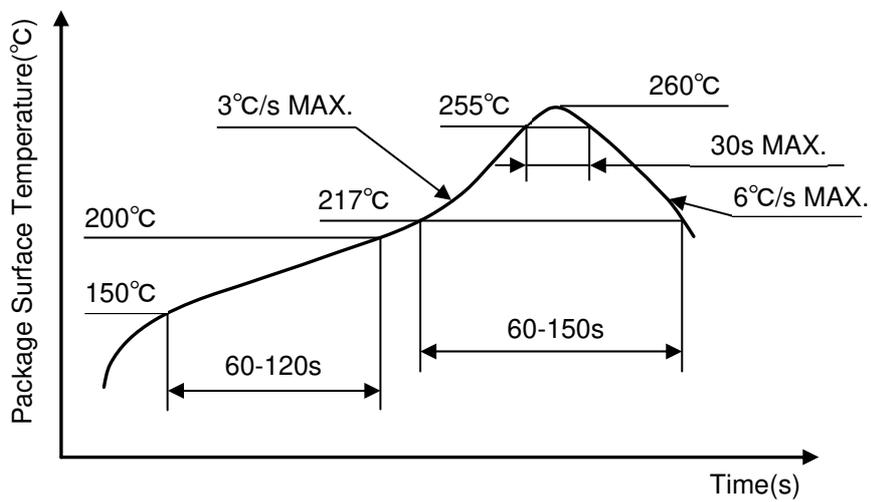
- Peeling angle 165 to 180° degrees to the taped surface.
- Peeling speed 300mm/min
- Peeling strength 0.1 to 1.0N



(5) Packing state



■ HEAT-RESISTANCE PROFILES



Reflow profile

Visual Inspection Criteria

| No. | Inspection Items | Inspection Criteria | Figures |
|-----|------------------------------|---|---------|
| 1 | Package chipping | <p>$A \geq 0.2\text{mm}$ is rejected</p> <p>$B \geq 0.2\text{mm}$ is rejected</p> <p>$C \geq 0.2\text{mm}$ is rejected</p> <p>And, Package chipping to Si surface and to bump is rejected.</p> | |
| 2 | Si surface chipping | <p>$A \geq 0.2\text{mm}$ is rejected</p> <p>$B \geq 0.2\text{mm}$ is rejected</p> <p>$C \geq 0.2\text{mm}$ is rejected</p> <p>But, even if $A \geq 0.2\text{mm}$, $B \leq 0.1\text{mm}$ is acceptable.</p> | |
| 3 | No bump | No bump is rejected. | |
| 4 | Marking miss | To reject incorrect marking, such as another product name marking or another lot No. marking. | |
| 5 | No marking | To reject no marking on the package. | |
| 6 | Reverse direction of marking | To reject reverse direction of marking character. | |
| 7 | Defective marking | To reject unreadable marking. (Microscope: X15/ White LED/ Viewed from vertical direction) | |
| 8 | Scratch | To reject unreadable marking character by scratch. (Microscope: X15/ White LED/ Viewed from vertical direction) | |
| 9 | Stain and Foreign material | To reject unreadable marking character by stain and foreign material. (Microscope: X15/ White LED/ Viewed from vertical direction) | |

1. The products and the product specifications described in this document are subject to change or discontinuation of production without notice for reasons such as improvement. Therefore, before deciding to use the products, please refer to our sales representatives for the latest information thereon.
2. The materials in this document may not be copied or otherwise reproduced in whole or in part without the prior written consent of us.
3. This product and any technical information relating thereto are subject to complementary export controls (so-called KNOW controls) under the Foreign Exchange and Foreign Trade Law, and related politics ministerial ordinance of the law. (Note that the complementary export controls are inapplicable to any application-specific products, except rockets and pilotless aircraft, that are insusceptible to design or program changes.) Accordingly, when exporting or carrying abroad this product, follow the Foreign Exchange and Foreign Trade Control Law and its related regulations with respect to the complementary export controls.
4. The technical information described in this document shows typical characteristics and example application circuits for the products. The release of such information is not to be construed as a warranty of or a grant of license under our or any third party's intellectual property rights or any other rights.
5. The products listed in this document are intended and designed for use as general electronic components in standard applications (office equipment, telecommunication equipment, measuring instruments, consumer electronic products, amusement equipment etc.). Those customers intending to use a product in an application requiring extreme quality and reliability, for example, in a highly specific application where the failure or misoperation of the product could result in human injury or death should first contact us.
 - Aerospace Equipment
 - Equipment Used in the Deep Sea
 - Power Generator Control Equipment (nuclear, steam, hydraulic, etc.)
 - Life Maintenance Medical Equipment
 - Fire Alarms / Intruder Detectors
 - Vehicle Control Equipment (automotive, airplane, railroad, ship, etc.)
 - Various Safety Devices
 - Traffic control system
 - Combustion equipment

In case your company desires to use this product for any applications other than general electronic equipment mentioned above, make sure to contact our company in advance. Note that the important requirements mentioned in this section are not applicable to cases where operation requirements such as application conditions are confirmed by our company in writing after consultation with your company.

6. We are making our continuous effort to improve the quality and reliability of our products, but semiconductor products are likely to fail with certain probability. In order to prevent any injury to persons or damages to property resulting from such failure, customers should be careful enough to incorporate safety measures in their design, such as redundancy feature, fire containment feature and fail-safe feature. We do not assume any liability or responsibility for any loss or damage arising from misuse or inappropriate use of the products.
7. The products have been designed and tested to function within controlled environmental conditions. Do not use products under conditions that deviate from methods or applications specified in this datasheet. Failure to employ the products in the proper applications can lead to deterioration, destruction or failure of the products. We shall not be responsible for any bodily injury, fires or accident, property damage or any consequential damages resulting from misuse or misapplication of the products.
8. **Quality Warranty**
 - 8-1. **Quality Warranty Period**

In the case of a product purchased through an authorized distributor or directly from us, the warranty period for this product shall be one (1) year after delivery to your company. For defective products that occurred during this period, we will take the quality warranty measures described in section 8-2. However, if there is an agreement on the warranty period in the basic transaction agreement, quality assurance agreement, delivery specifications, etc., it shall be followed.
 - 8-2. **Quality Warranty Remedies**

When it has been proved defective due to manufacturing factors as a result of defect analysis by us, we will either deliver a substitute for the defective product or refund the purchase price of the defective product.

Note that such delivery or refund is sole and exclusive remedies to your company for the defective product.
 - 8-3. **Remedies after Quality Warranty Period**

With respect to any defect of this product found after the quality warranty period, the defect will be analyzed by us. On the basis of the defect analysis results, the scope and amounts of damage shall be determined by mutual agreement of both parties. Then we will deal with upper limit in Section 8-2. This provision is not intended to limit any legal rights of your company.
9. Anti-radiation design is not implemented in the products described in this document.
10. The X-ray exposure can influence functions and characteristics of the products. Confirm the product functions and characteristics in the evaluation stage.
11. WLCSP products should be used in light shielded environments. The light exposure can influence functions and characteristics of the products under operation or storage.
12. Warning for handling Gallium and Arsenic (GaAs) products (Applying to GaAs MMIC, Photo Reflector). These products use Gallium (Ga) and Arsenic (As) which are specified as poisonous chemicals by law. For the prevention of a hazard, do not burn, destroy, or process chemically to make them as gas or power. When the product is disposed of, please follow the related regulation and do not mix this with general industrial waste or household waste.
13. Please contact our sales representatives should you have any questions or comments concerning the products or the technical information.



Nisshinbo Micro Devices Inc.

Official website

<https://www.nisshinbo-microdevices.co.jp/en/>

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